

Compal Confidential

Schematics Document

Intel Huron River

Sandy Bridge with Cougar Point core logic

2010-10-27

REV:1.0

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				Size	Document Number
				Custom	LA-6931P
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Model Name : P5LM0
File Name : LA-6931P

Security Classification		Compal Secret Data		Compal Electronics, Inc.		
Issued Date	2009/08/25	Deciphered Date	2010/08/25	Title Block Diagrams		
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Voltage Rails

Power Plane	Description	S1	S3	S5	DGPU (DIS)	IGPU (SG)
VIN	Adapter power supply (19V)	N/A	N/A	N/A		
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A		
BATT+	Battery power supply (12.6V)	N/A	N/A	N/A		
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF		
+0.75VS	0.75V switched power rail for DDR terminator	ON	OFF	OFF		
+1.05VS_VCCP	1.05V switched power rail for CPU (PCH)	ON	OFF	OFF		
+VGFX_CORE	Core voltage for IGPU	ON	OFF	OFF		
+1.5V	1.5V power rail for DDRIII	ON	ON	OFF		
+1.5VS	1.5V switched power rail	ON	OFF	OFF		
+1.8VS	1.8V switched power rail	ON	OFF	OFF		
+3VALW	3.3V always on power rail	ON	ON	ON*		
+3VALW_PCH	3.3V power rail for PCH	ON	ON	ON*		
+LAN_JO	3.3V power rail for LAN	ON	ON	ON*		
+3VS	3.3V switched power rail	ON	OFF	OFF		
+5VALW	5V always on power rail	ON	ON	ON*		
+5VS	5V switched power rail	ON	OFF	OFF		
+VSB	VSB always on power rail	ON	ON	ON*		
+RTCVCC	RTC power	ON	ON	ON		
+VGA_CORE	5V power rail for GPU	ON	OFF	OFF	ON	OFF
+1.5VSDGPU	1.5V power rail for VRAM	ON	OFF	OFF	ON	OFF
+1.8VSDGPU	1.8V switched power rail for GPU	ON	OFF	OFF	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
--------	--------	-----------	------------

EC SM Bus1 address

EC SM Bus2 address

Device	Address	Device	Address
--------	---------	--------	---------

Ibex SM Bus address

Device	Address
--------	---------

STATE	SIGNAL				SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON	HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON				
S1 (Power On Suspend)	LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW				
S3 (Suspend to RAM)	LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF				
S4 (Suspend to Disk)	LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF				
S5 (Soft OFF)	LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF				

Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 5%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
* 3	0.4
4	
5	
6	
7	

BTO Option Table

BTO Item	BOM Structure
DIS Only	DIS@
Switchable Graphics	SG@
Granville	GRA@
Whistler	WHI@
For CIR	CIR@
USB2.0 bus	USB2@
DDR M1	M1@
DDR M3	M3@
For 45 level	45@

USB Port Table

USB 2.0	USB 1.1	Port	4 External USB Port
EHCI1	UHCI0	0	USB/B
		1	USB Conn.
	UHCI1	2	
		3	
	UHCI2	4	Mini Card 1
		5	Mini Card 2
EHCI2	UHCI3	6	
		7	
	UHCI4	8	USB Conn.
		9	eSATA USB
	UHCI5	10	CMOS Camera
		11	Finger Print
	UHCI6	12	USB3.0 @
		13	Blue Tooth

43 Level BOM Config

Granville DIS: GRA@ DIS@ CIR@ M1@
Whistler SG: WHI@ SG@ CIR@ M1@

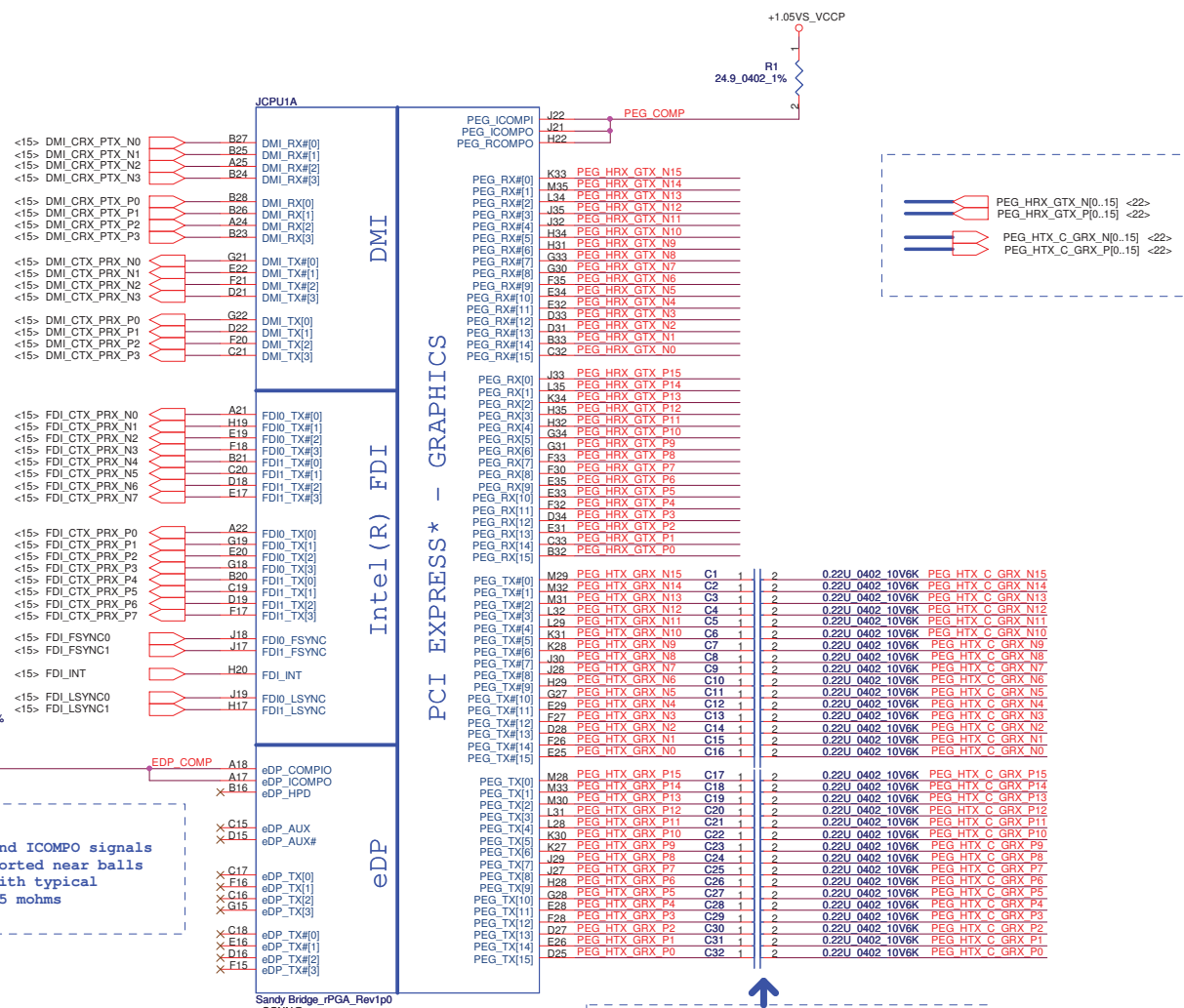
45 Level BOM Config

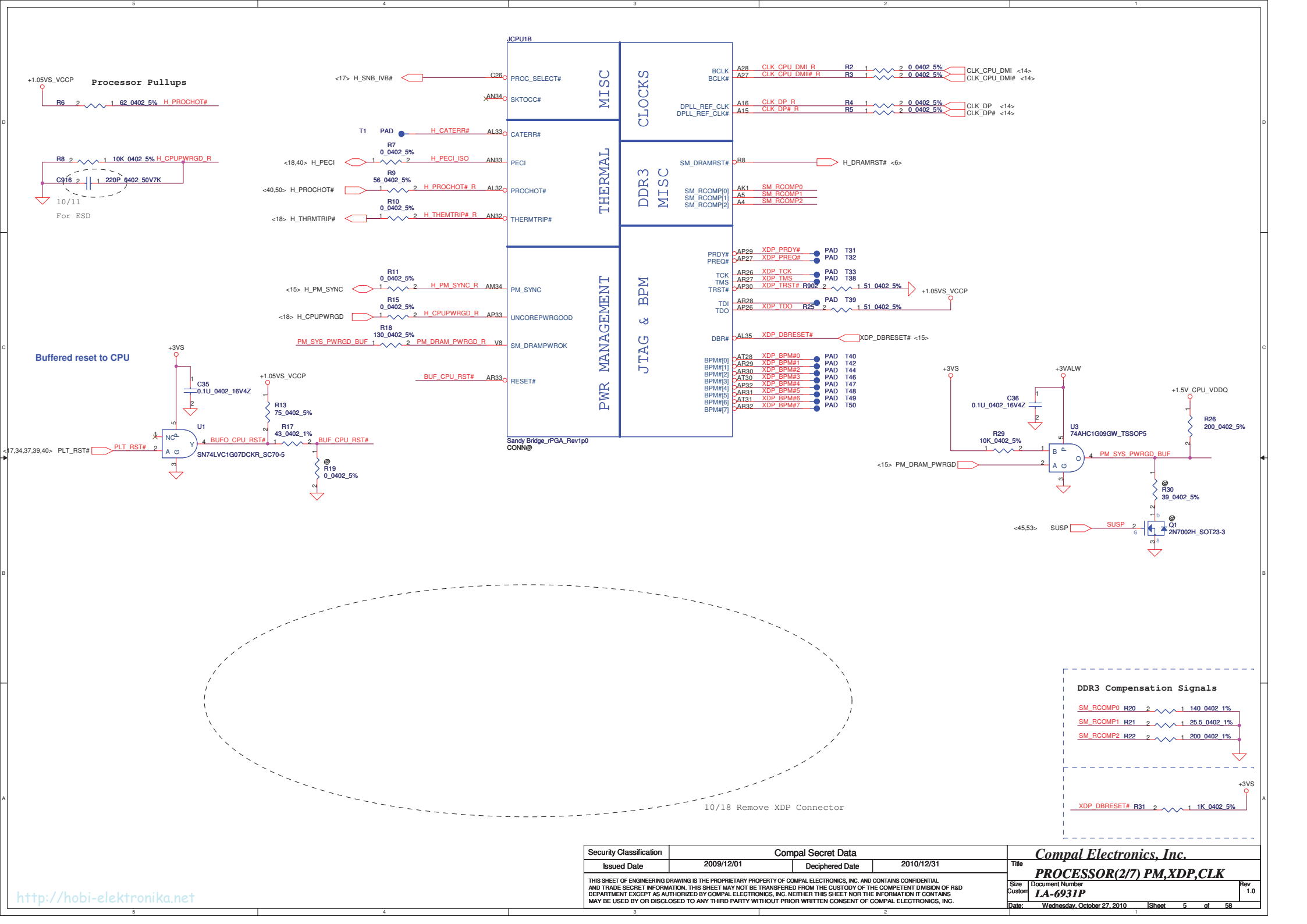
45@

VRAM BOM Config

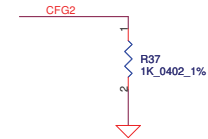
X76255BOL01: HYNIX 1G (old die)
X76255BOL02 HYNIX 1G (new die)
X76255BOL04 HYNIX 2G

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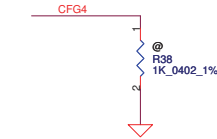




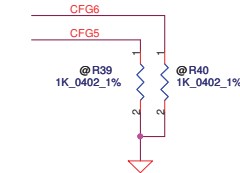
CFG Straps for Processor



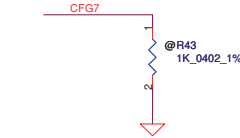
PEG Static Lane Reversal - CFG2 is for the 16x	
CFG2	1: Normal Operation; Lane # definition matches socket pin map definition * 0: Lane Reversed



Display Port Presence Strap	
CFG4	1 : Disabled; No Physical Display Port attached to Embedded Display Port * 0 : Enabled; An external Display Port device is connected to the Embedded Display Port

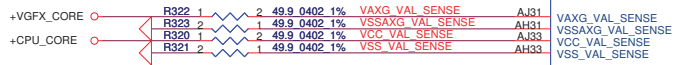


PCIe Port Bifurcation Straps	
CFG[6:5]	* 11: (Default) x16 - Device 1 functions 1 and 2 disabled 10: x8, x8 - Device 1 function 1 enabled ; function 2 disabled 01: Reserved - (Device 1 function 1 disabled ; function 2 enabled) 00: x8, x4, x4 - Device 1 functions 1 and 2 enabled

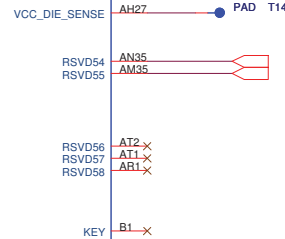


PEG DEFER TRAINING	
CFG7	1: (Default) PEG Train immediately following xxRESETB de assertion 0: PEG Wait for BIOS for training

T2	PAD	CFG0	AK28	CFG[0]
T3	PAD	CFG1	AK29	CFG[1]
T4	PAD	CFG2	AL26	CFG[2]
T5	PAD	CFG3	AL27	CFG[3]
T6	PAD	CFG4	AK26	CFG[4]
T7	PAD	CFG5	AL29	CFG[5]
T8	PAD	CFG6	AL30	CFG[6]
T9	PAD	CFG7	AM31	CFG[7]
T10	PAD	CFG8	AM32	CFG[8]
T11	PAD	CFG9	AM30	CFG[9]
T12	PAD	CFG10	AM28	CFG[10]
T13	PAD	CFG11	AM26	CFG[11]
T51	PAD	CFG12	AN28	CFG[12]
T52	PAD	CFG13	AN31	CFG[13]
T54	PAD	CFG14	AN26	CFG[14]
T58	PAD	CFG15	AM27	CFG[15]
T61	PAD	CFG16	AK31	CFG[16]
T64	PAD	CFG17	AN29	CFG[17]

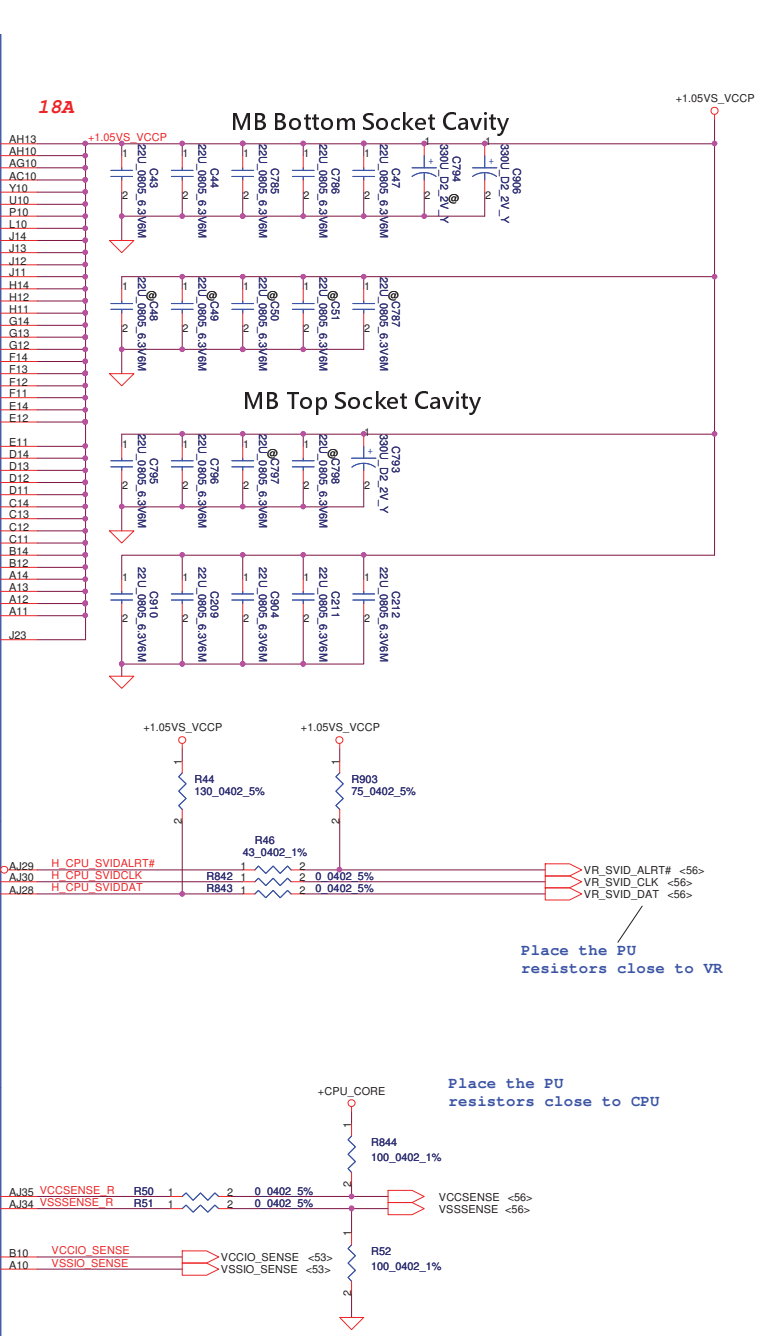
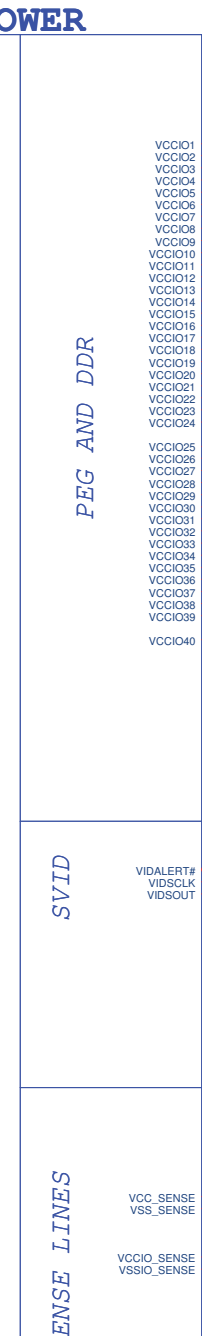
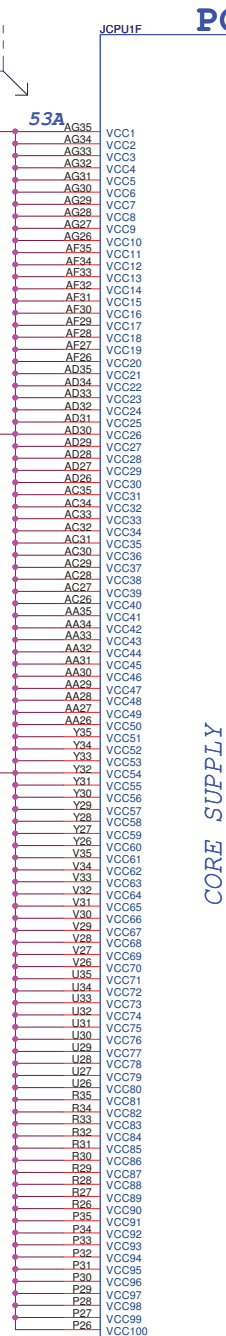
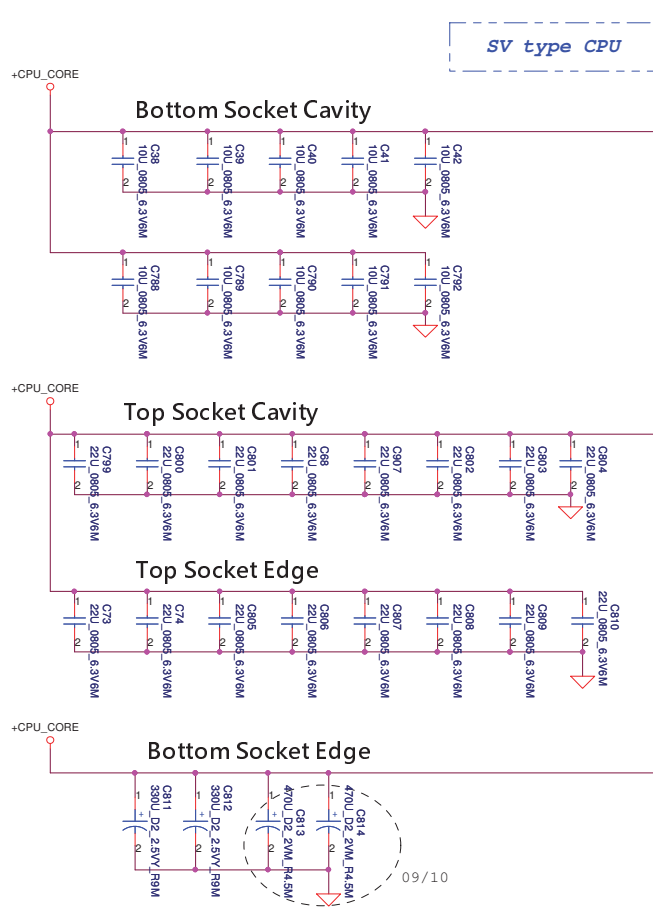


RESERVED



Sandy Bridge_rPGA_Rev1p0
CONN@

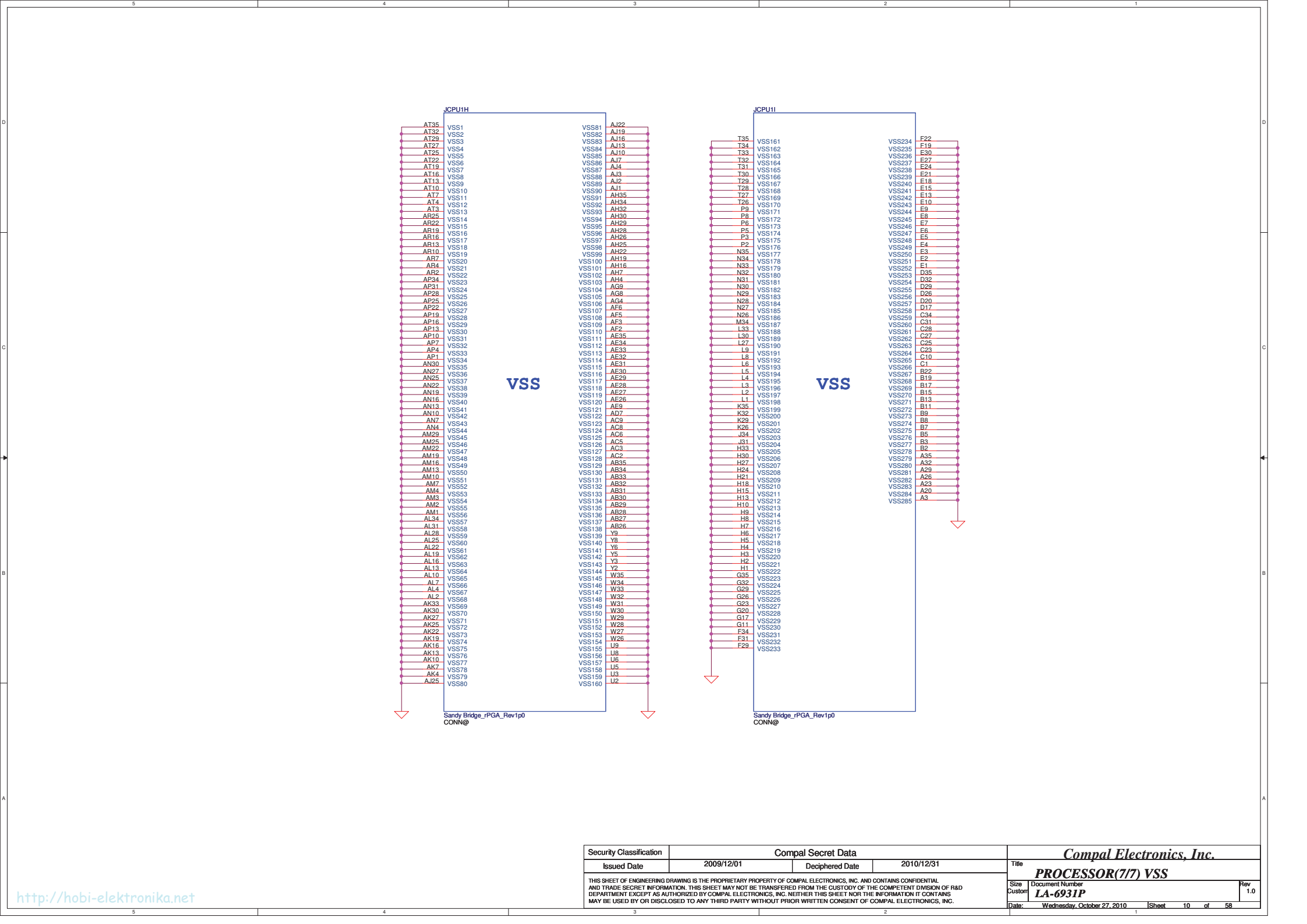
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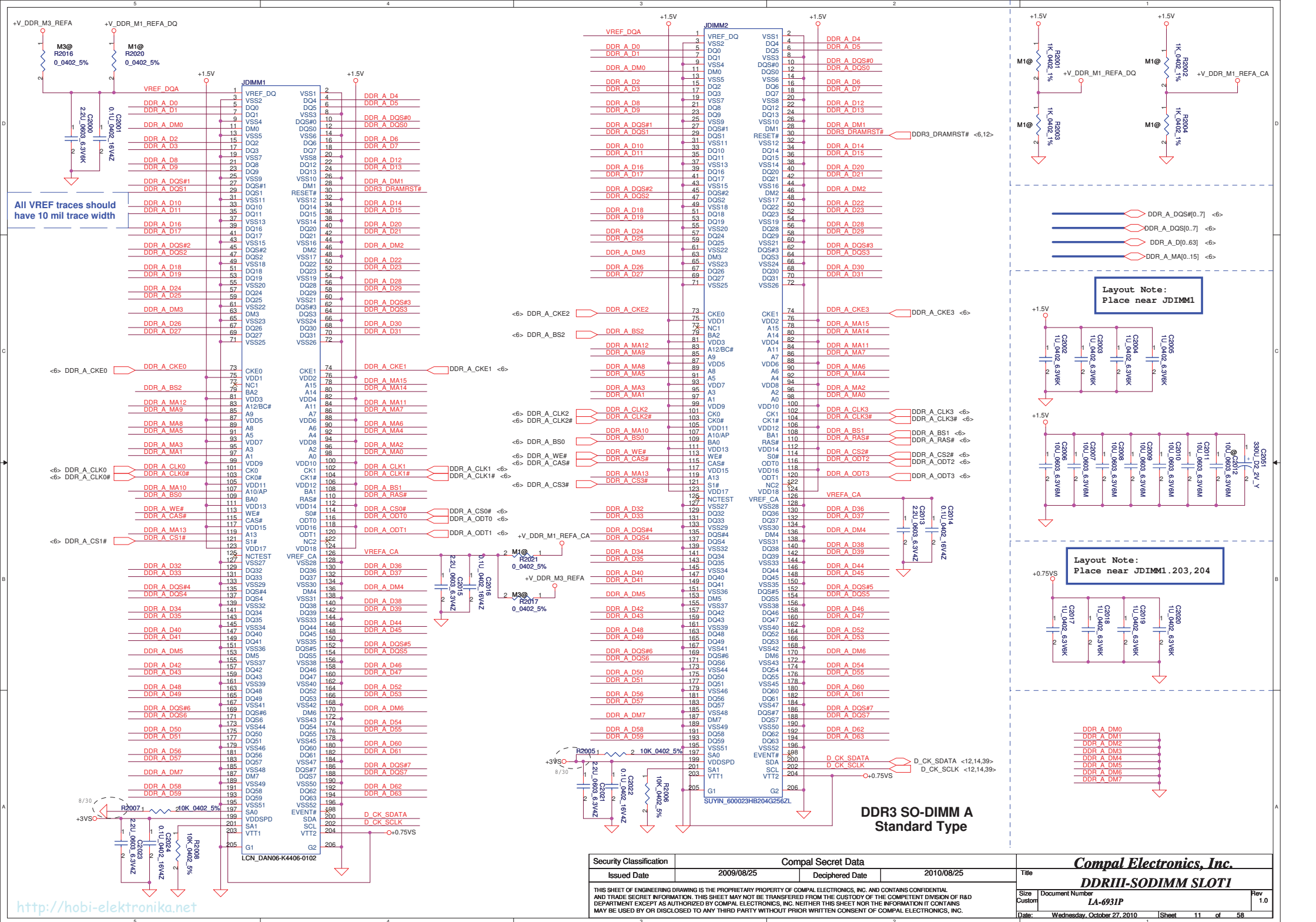


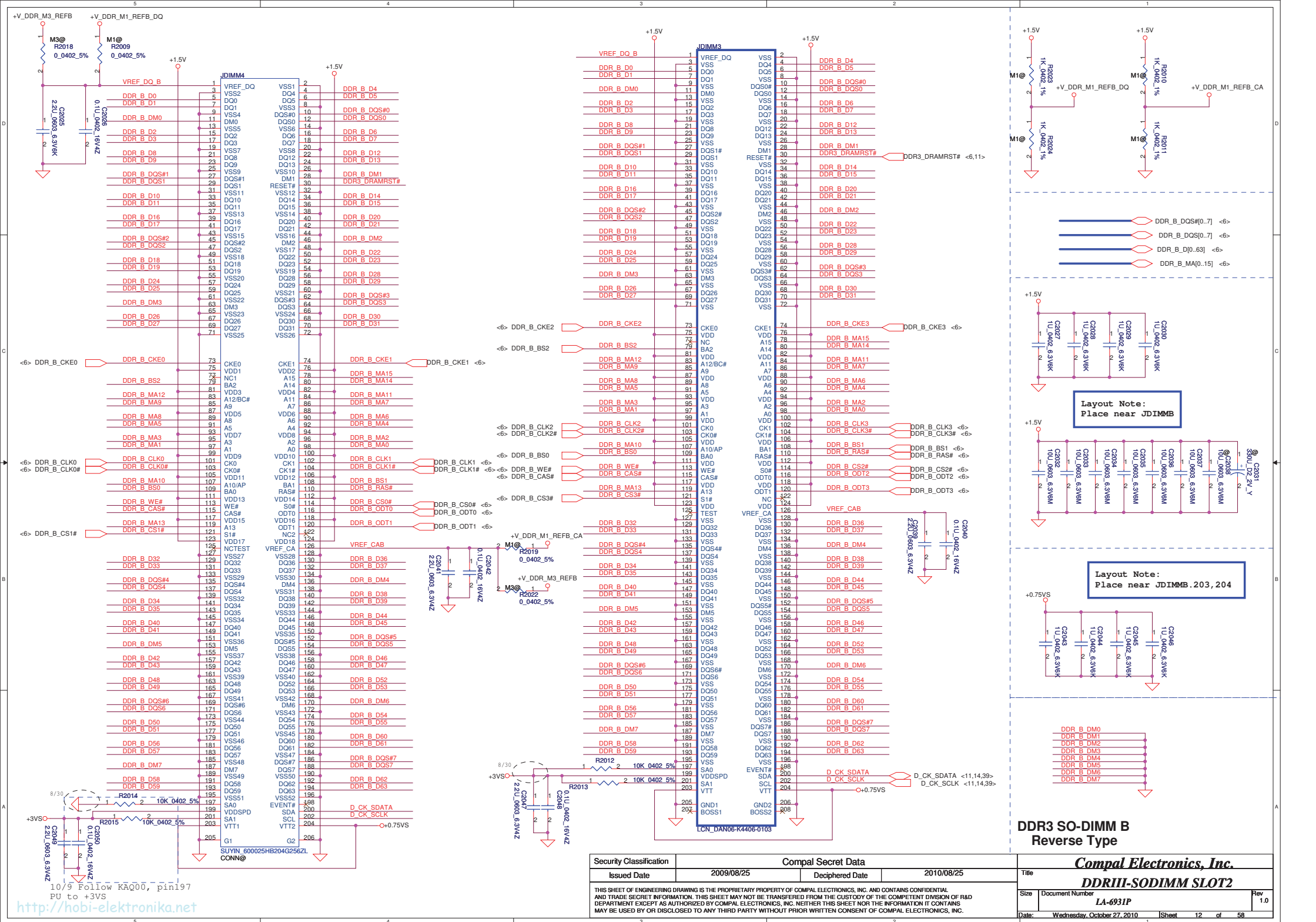
The schematic diagram illustrates the CPU1.5V_S3 power plane. It shows the connection of the CPU1.5V_S3_GATE and SUSP# signals to the power plane. The power plane is connected to +VSB, +1.5V, +1.5V_CPU_VDDQ, and +3VALW. The diagram includes components like resistors (R2300, R2301, R2302, R2303, R2304), capacitors (C2300, C2310), and transistors (Q2301A, Q2301B, Q2301C). The diagram is labeled "8/31 Intel".

[illegible]

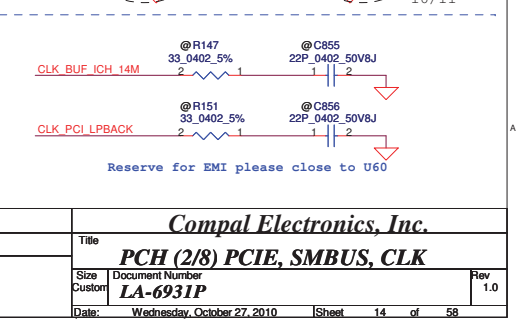
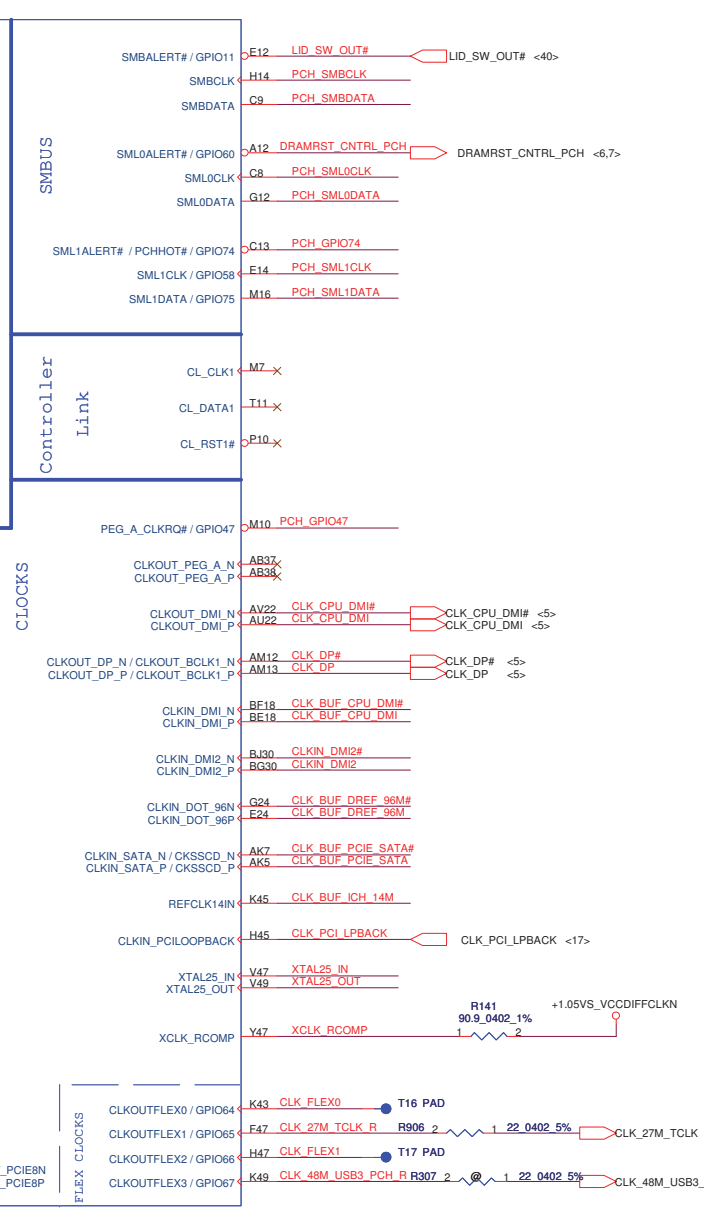
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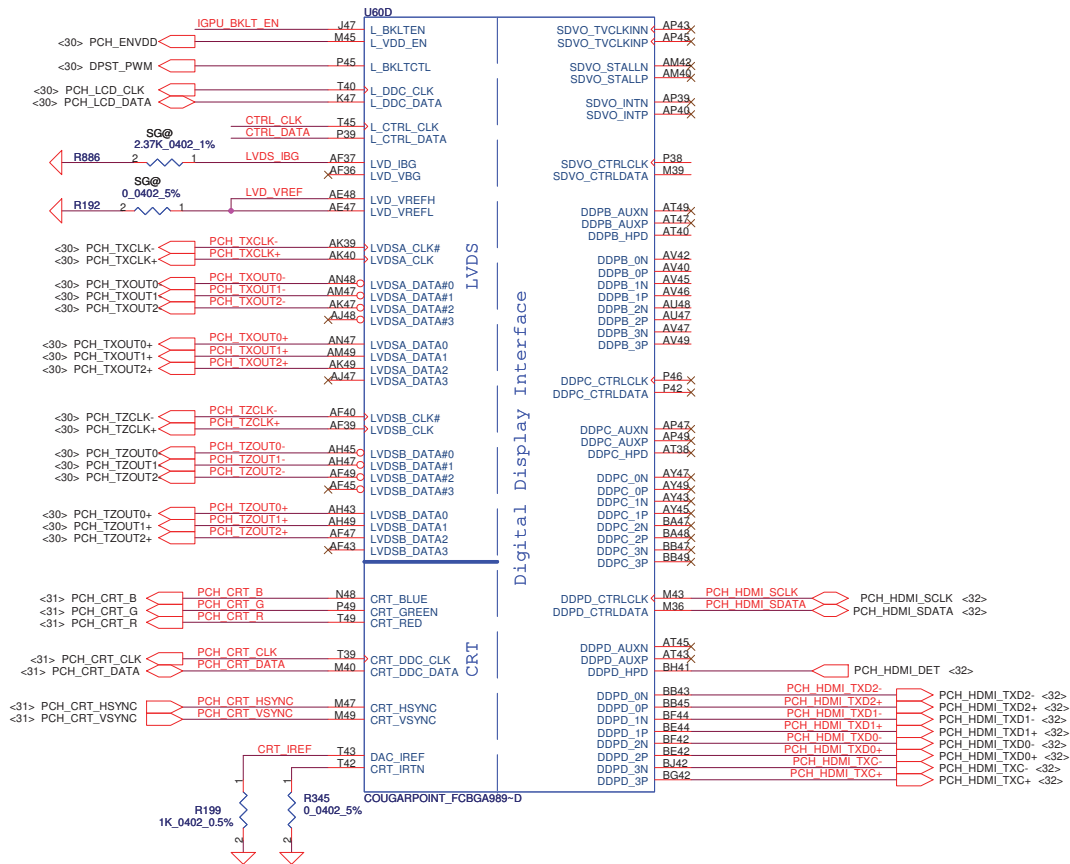
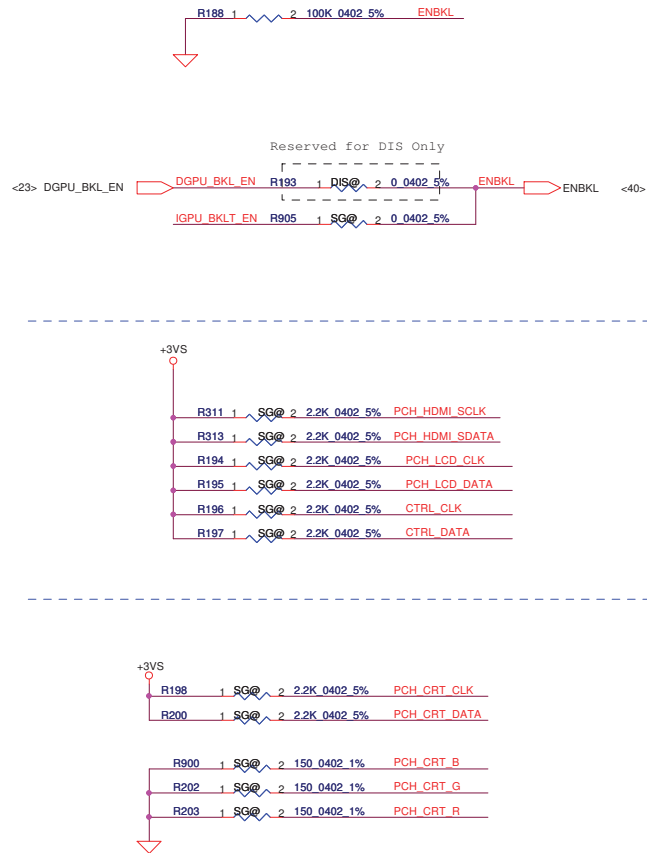




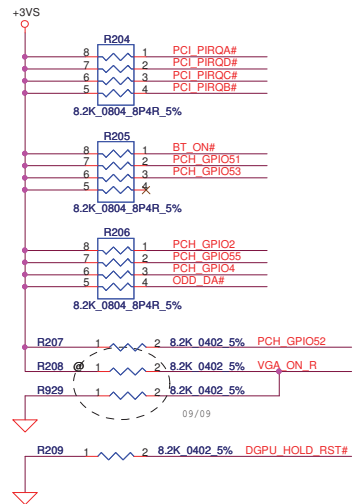




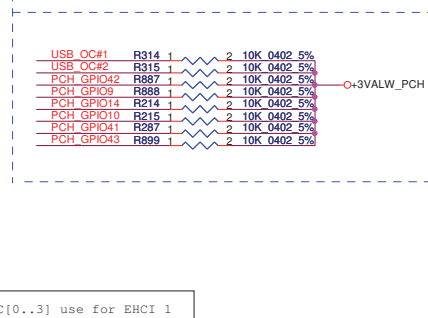
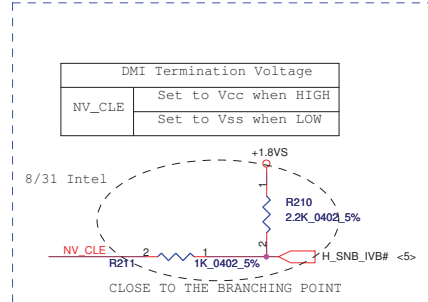
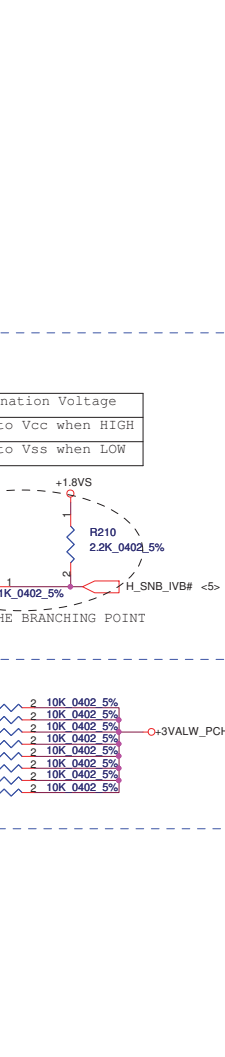
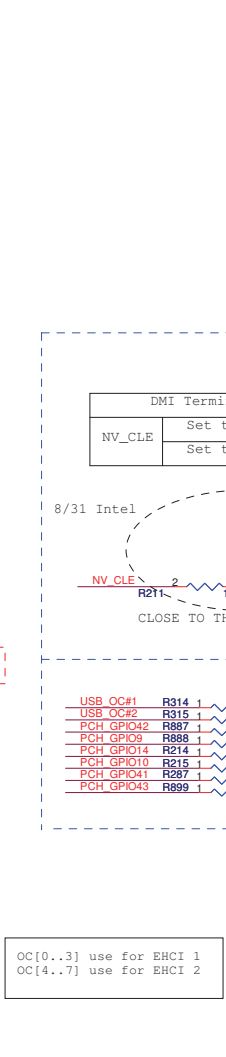
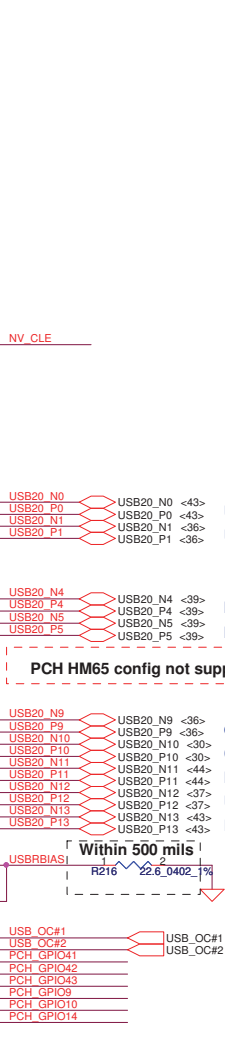
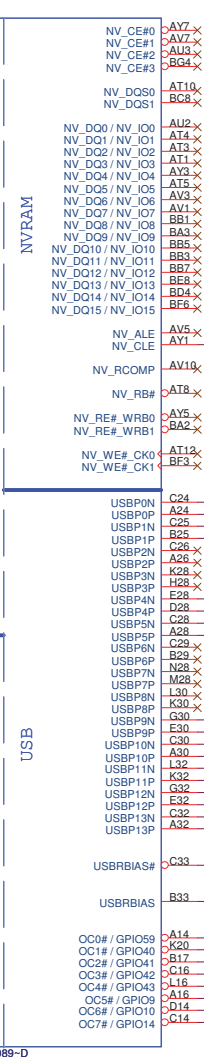
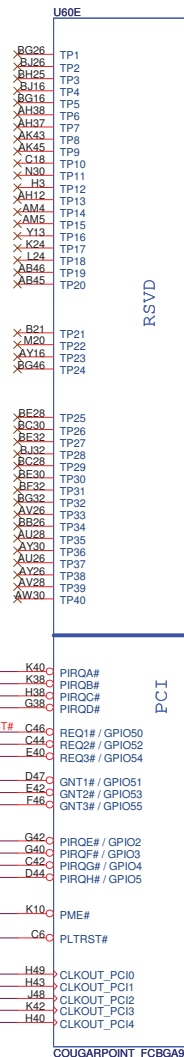
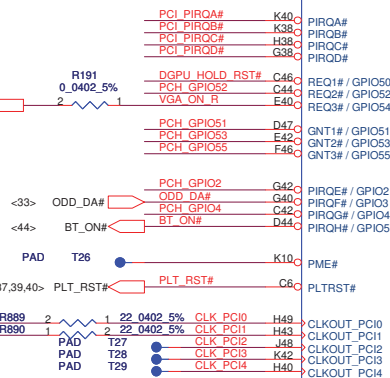
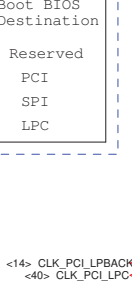
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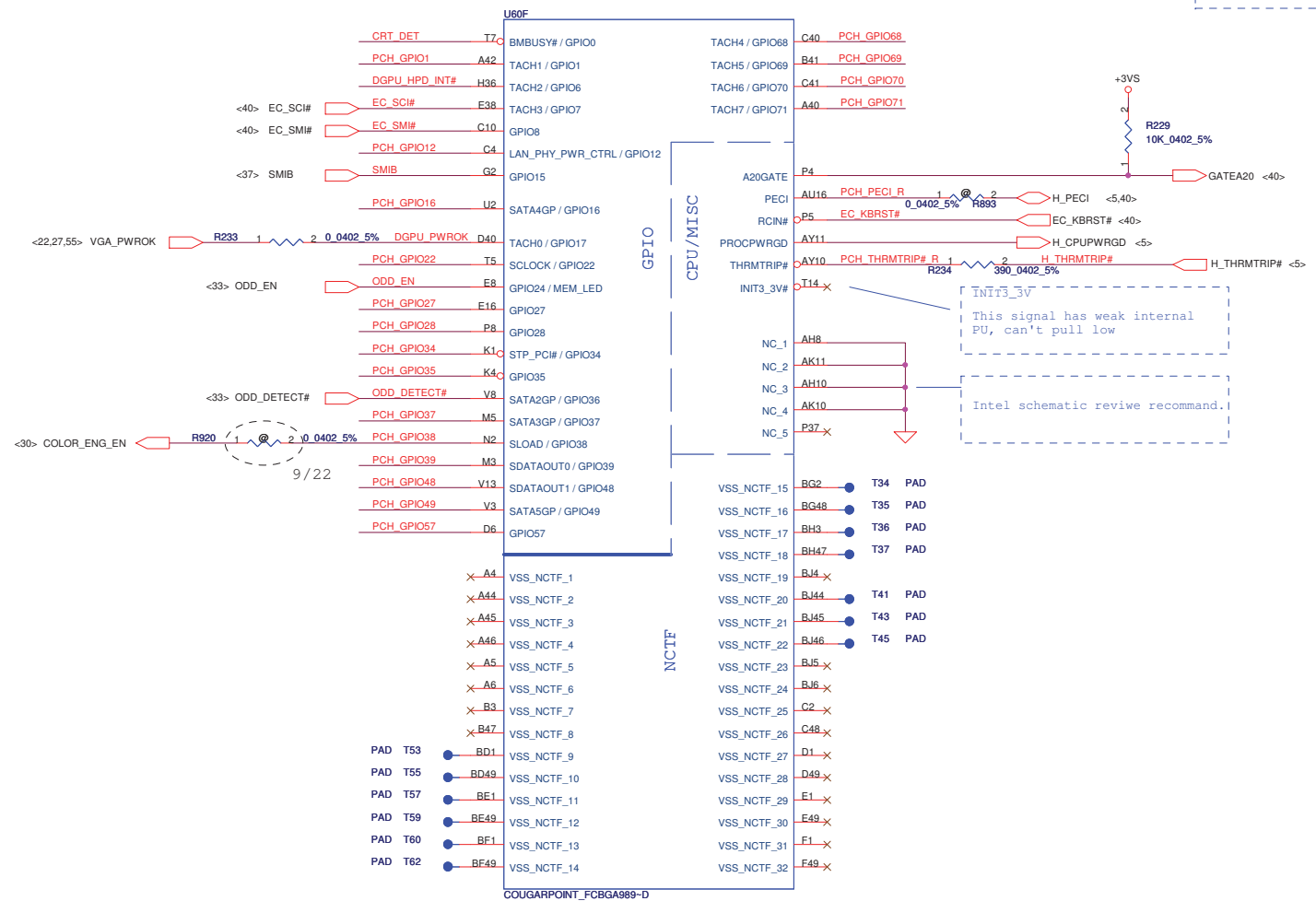
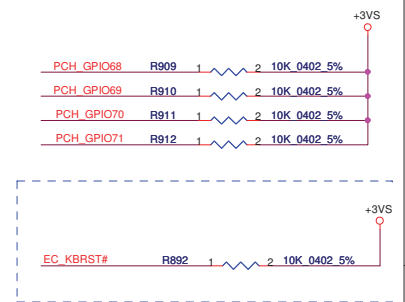
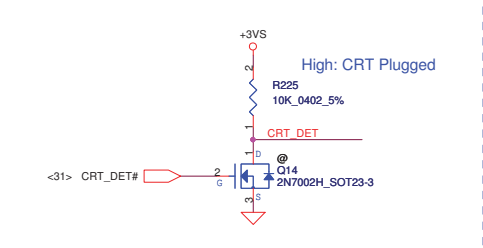
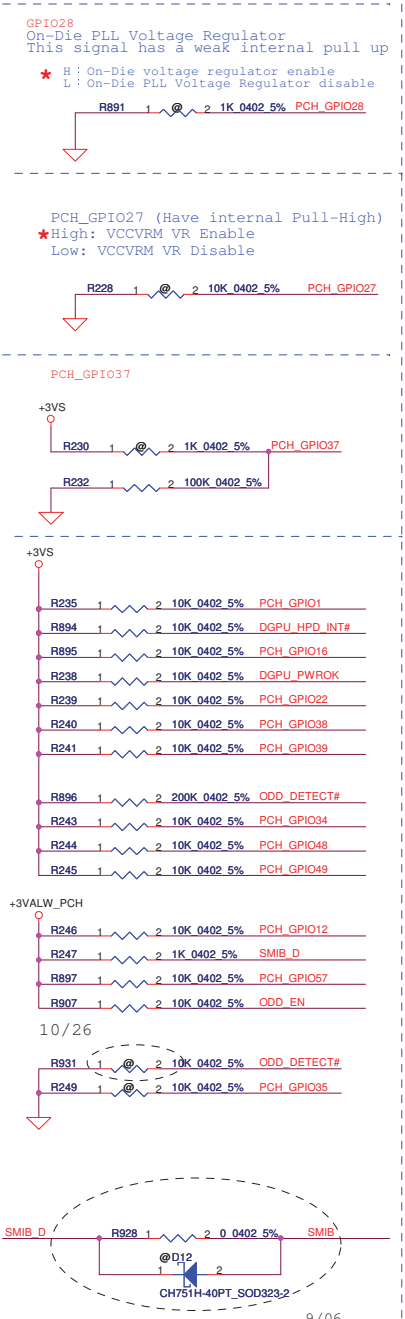


Boot BIOS Strap bit1 BBS1		
Bit11	Bit10	Boot BIOS Destination
GNT1# / GPIO51	0 1	Reserved
	1 0	PCI
	1 1	SPI
	0 0	LPC

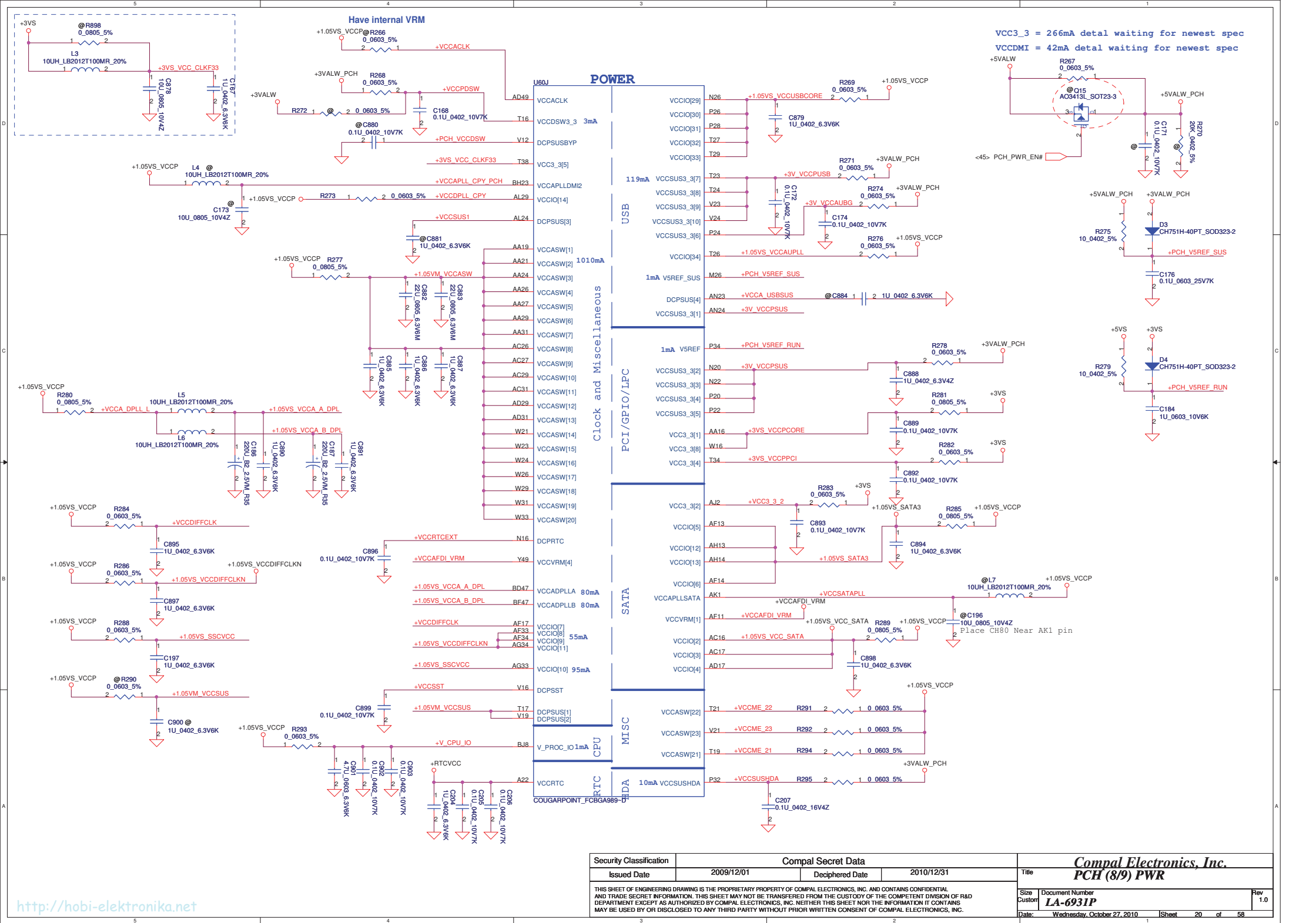


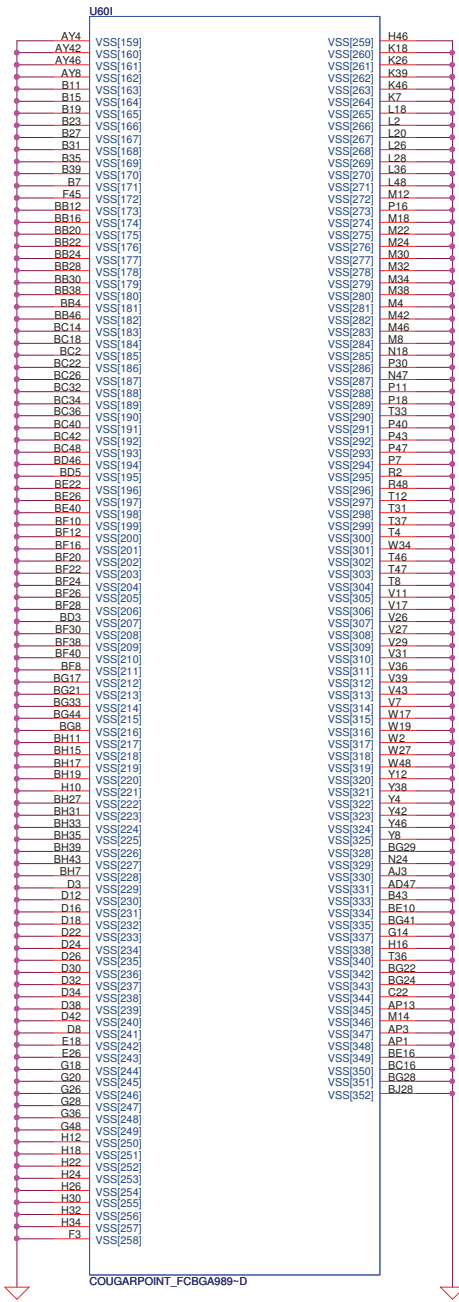
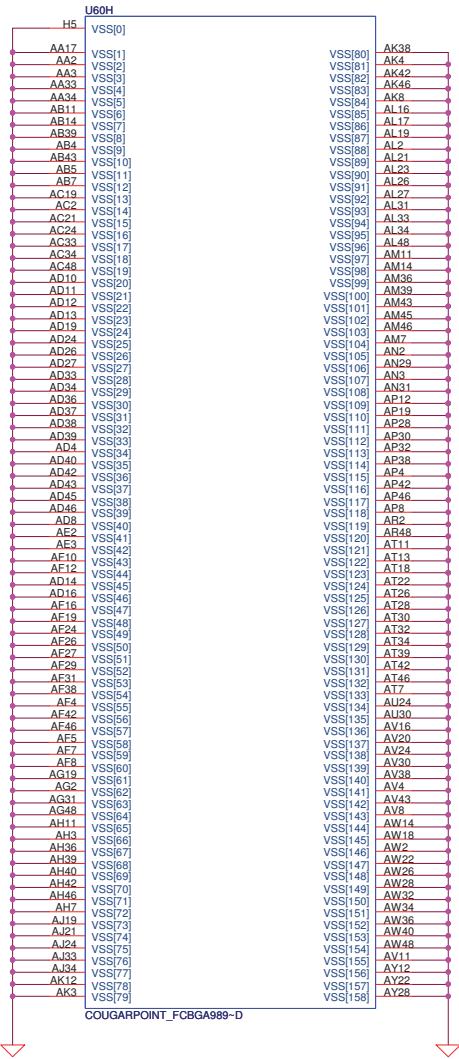
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Title		Compal Electronics, Inc.	
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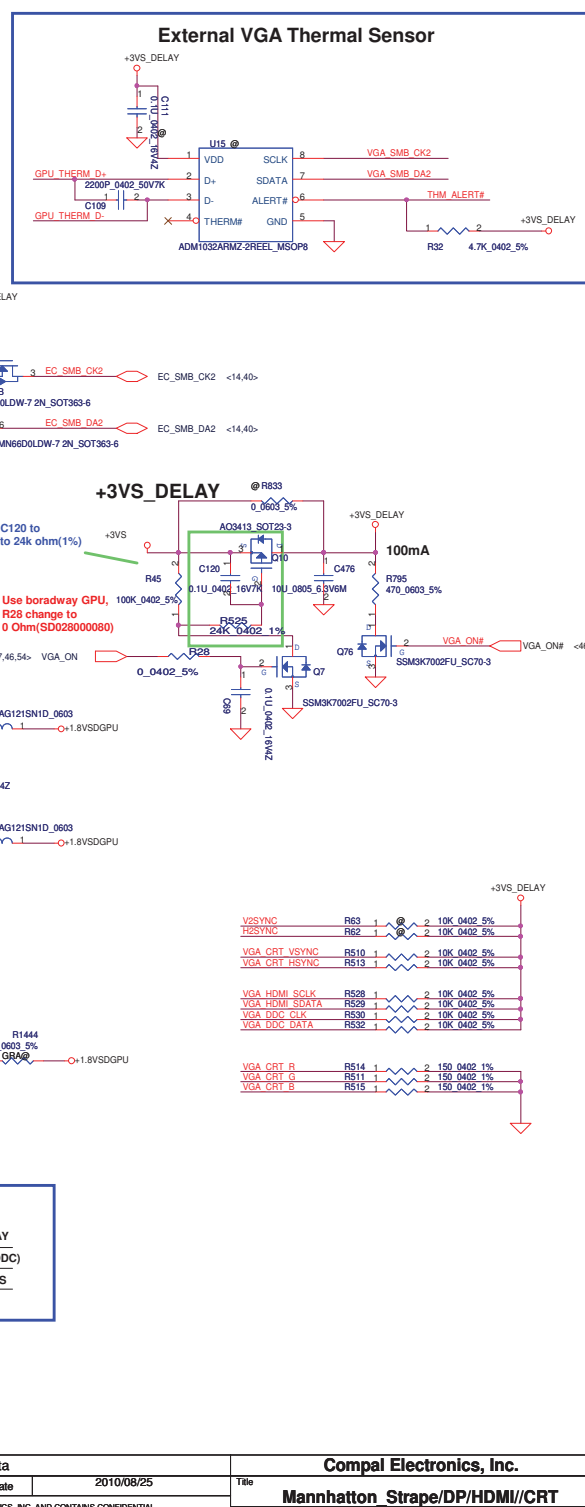
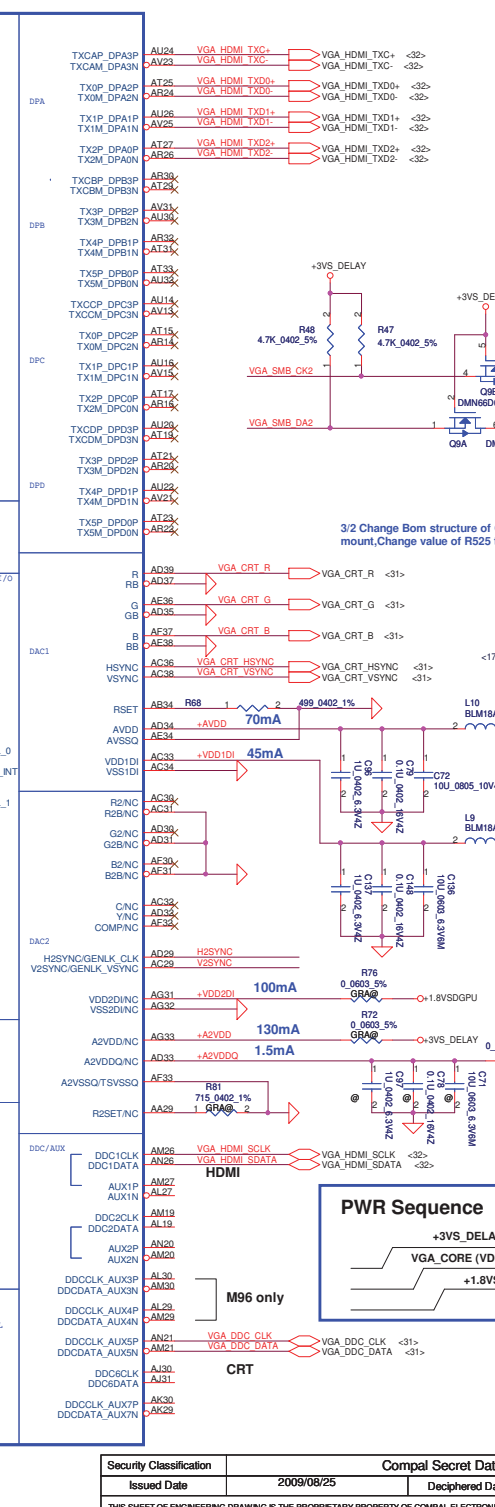
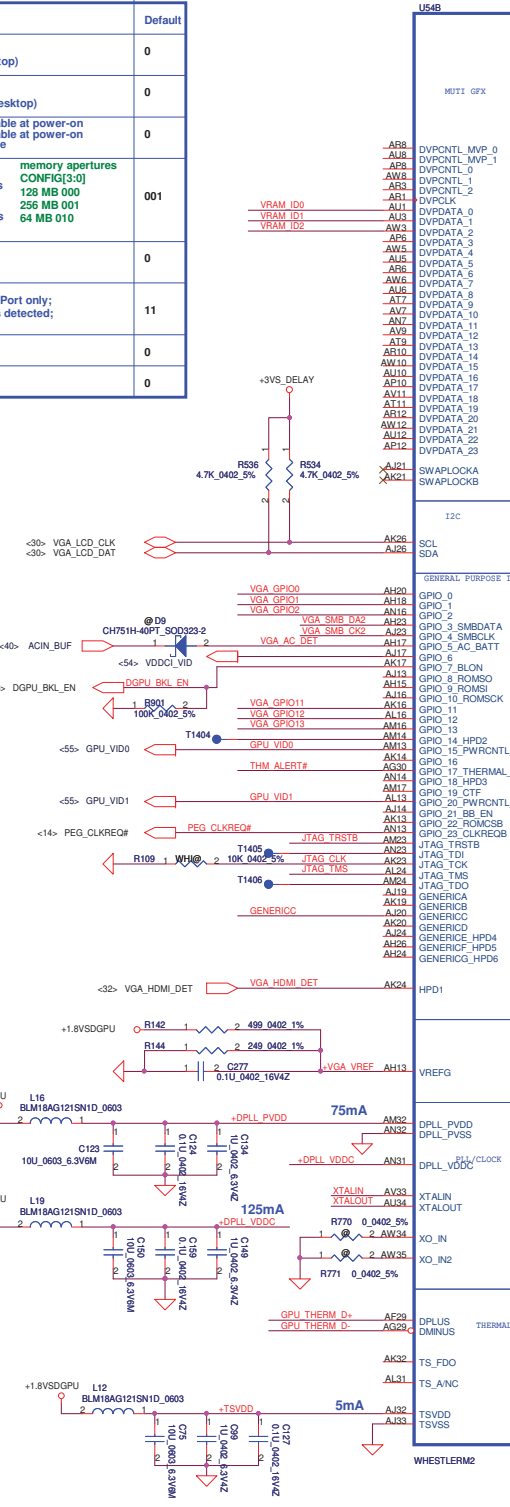
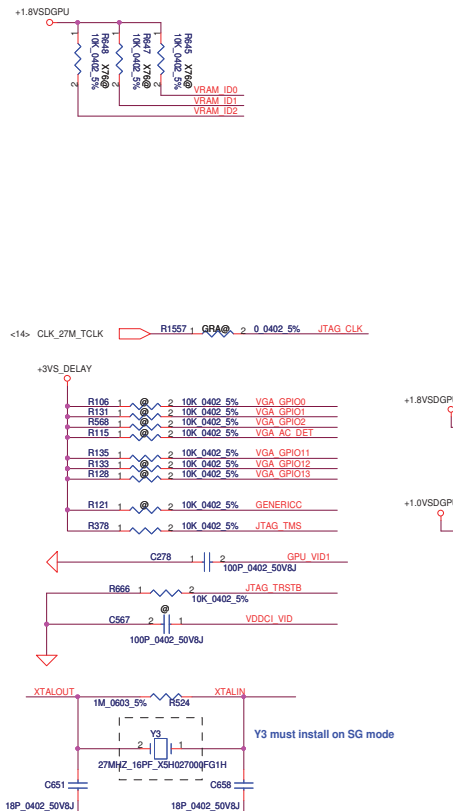




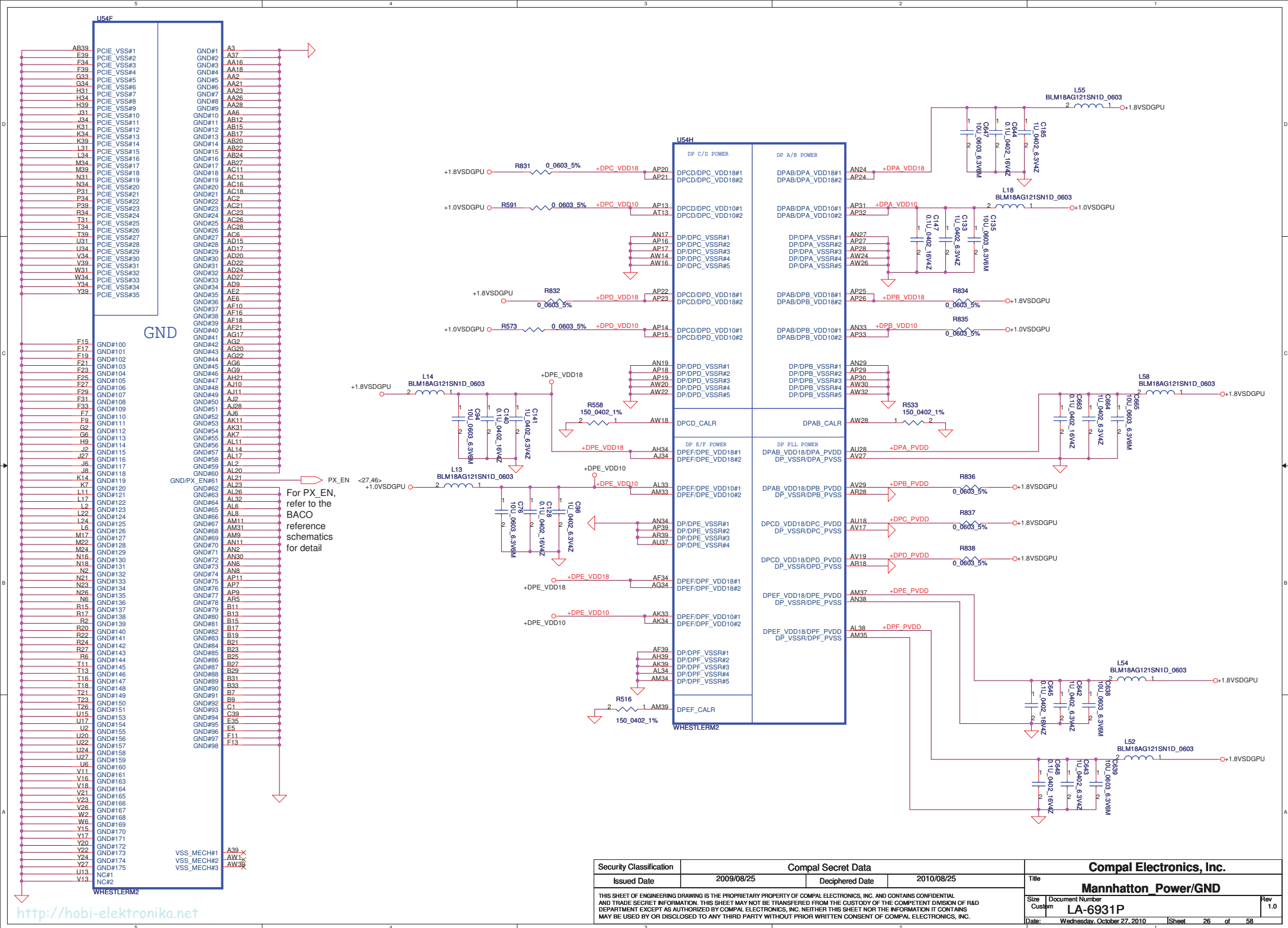
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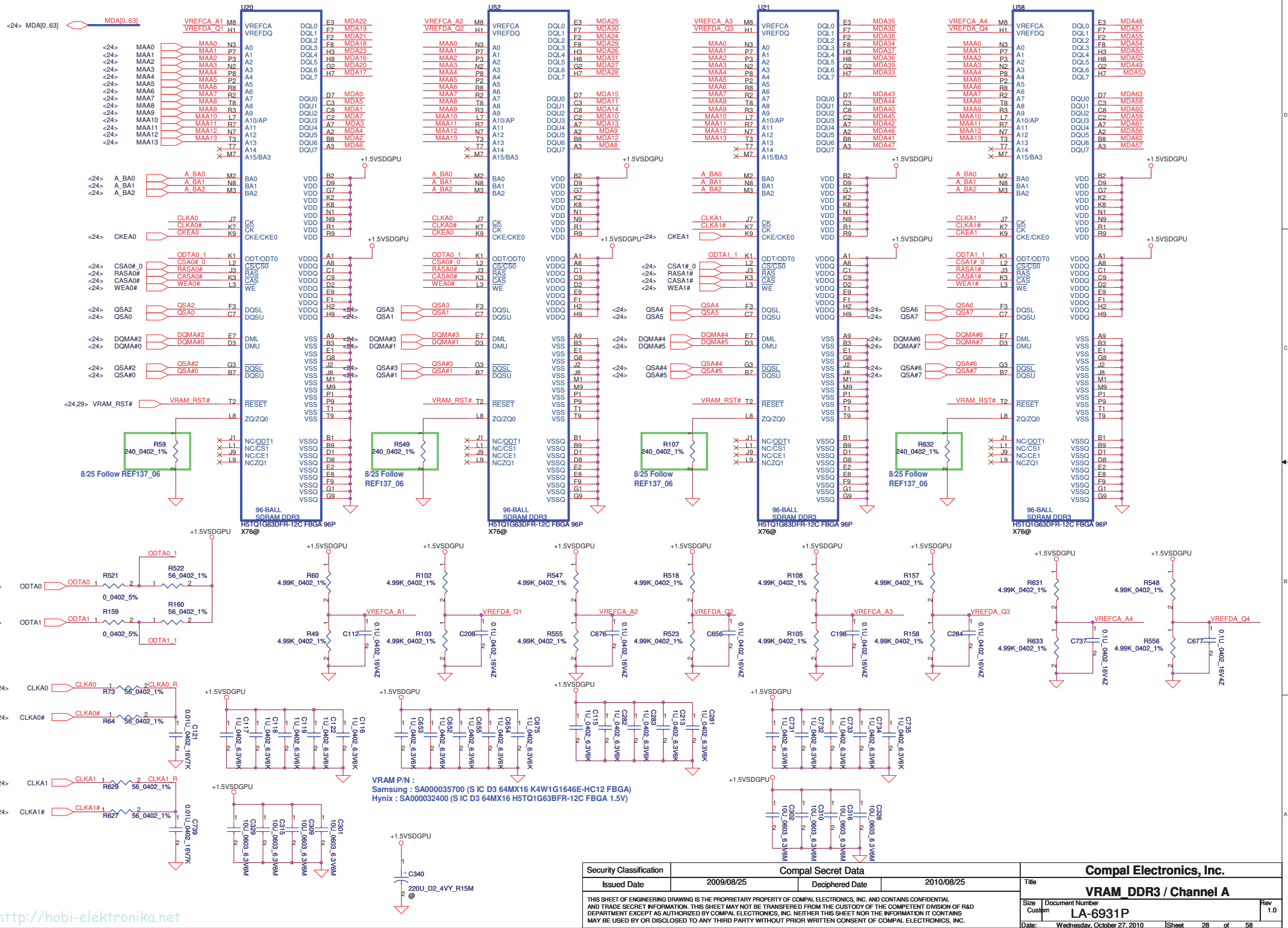
Strap Name		Pin Straps description	Default
TX_PWRS_ENB	GPIO0	Transmitter Power Saving Enable 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)	0
TX_DEEMPH_EN	GPIO1	PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for desktop)	0
BIF_GEN2_EN	GPIO2	0= Advertises the PCI-E device as 2.5 GT/s capable at power-on 1= Advertises the PCI-E device as 5.0 GT/s capable at power-on 5.0 GT/s capability will be controlled by software	0
CONFIG[1] CONFIG[2] CONFIG[0]	GPIO13 GPIO12 GPIO11	memory apertures CONFIG[3:0] a) If BIOS_ROM_EN = 1, then Config[2:0] defines the ROM type. b) If BIOS_ROM_EN = 0, then Config[2:0] defines the primary memory aperture size. 128 MB 000 256 MB 001 64 MB 010	001
BIOS_ROM_EN	GPIO22	Enable external BIOS ROM device 0: Disable, 1: Enable	0
AUD[1] AUD[0]	HSYNC VSYNC	00: No audio function; 01: Audio for DisplayPort only; 10: Audio for DisplayPort and HDMI if adapter is detected; 11: Audio for both DisplayPort and HDMI	11
SMS_EN_HARD	H2SYNC	Can be unconnected if not used.	0
VIP_DEVICE _STRAP_DIS	V2SYNC	Can be unconnected if not used.	0

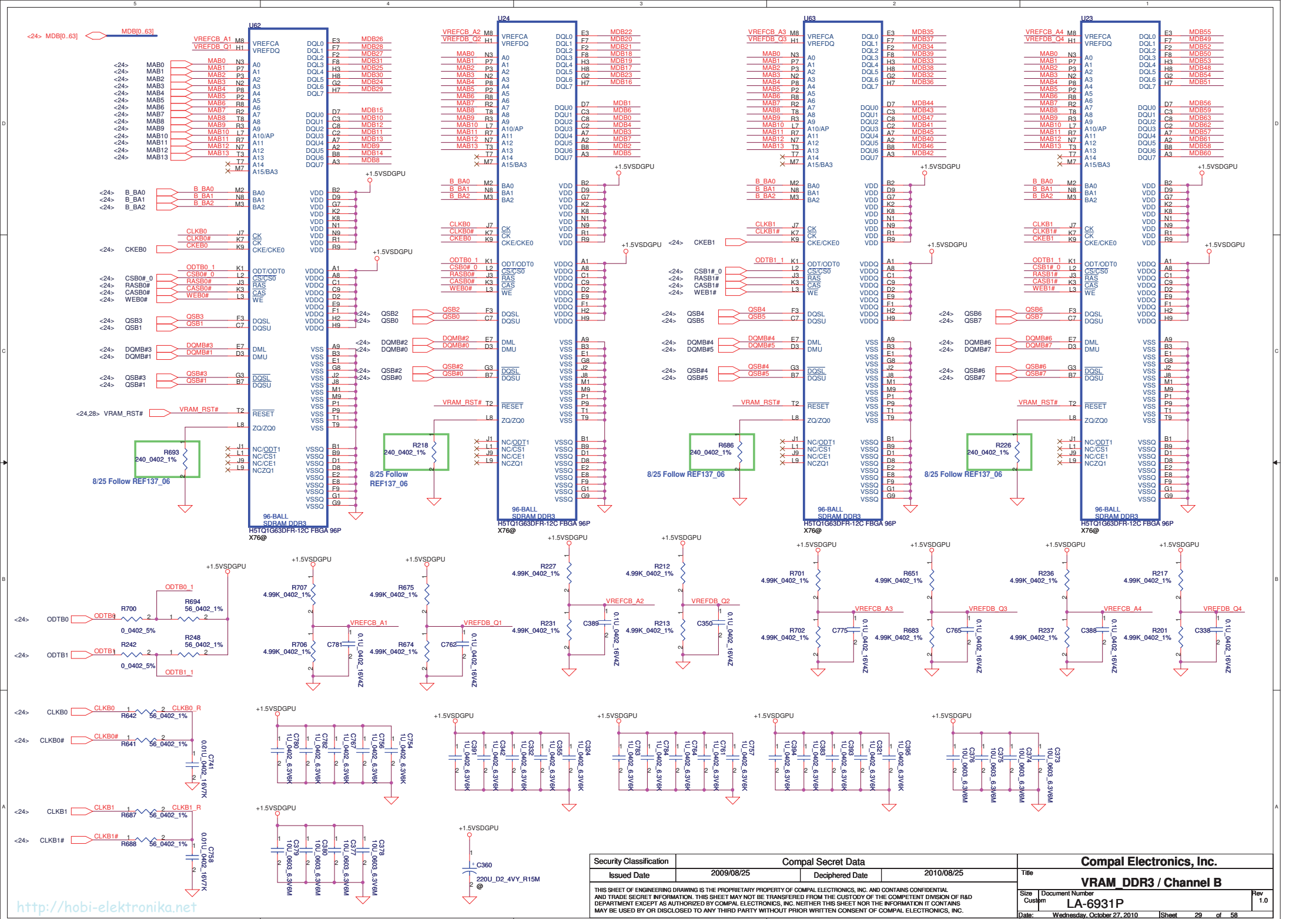
VRAM	Location	VRAM_ID2	VRAM_ID1	VRAM_ID0
Hynix(8pcs) 1GB (old) H5TQ1G63BFR-12C		0	0	1
Hynix(8pcs) 1GB (900MHz) H5TQ1G63DFR-11C		0	1	0
Samsung(8pcs) 1GB (900MHz) K4W1G1646G-BC11 LF		0	1	1
Hynix(8pcs) 2GB (800MHz) H5TQ2G63BFR-12C		1	0	0
Samsung(8pcs) 2GB (800MHz) K4W2G1646C-HC12		1	0	1



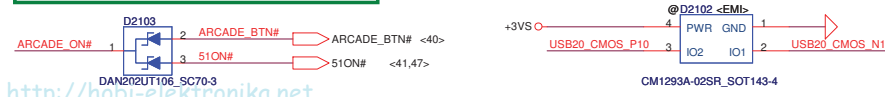
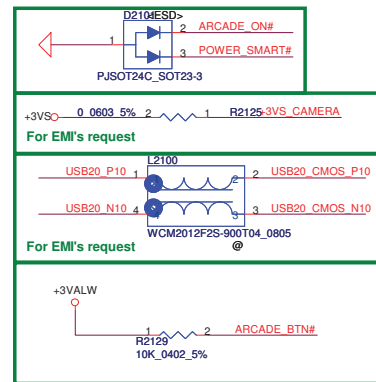
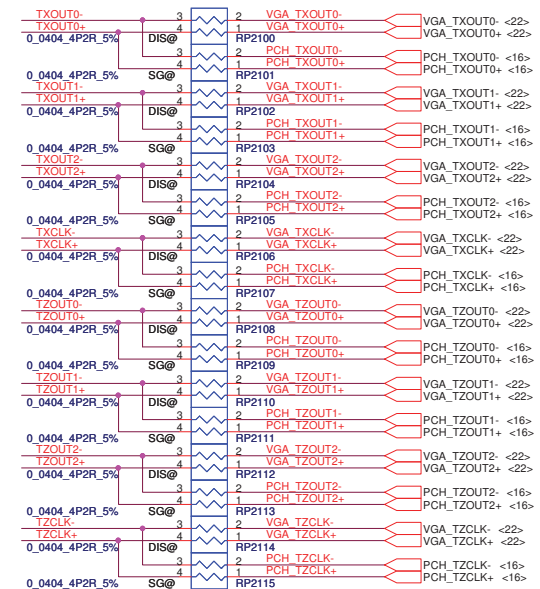
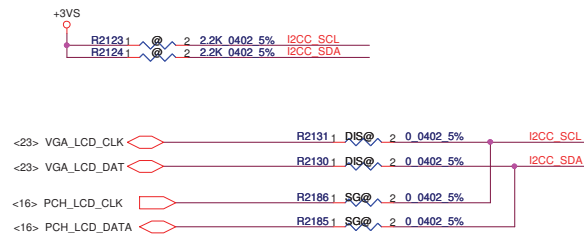
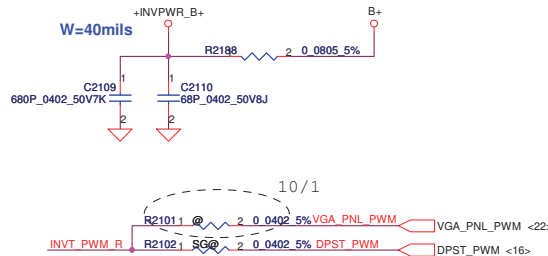
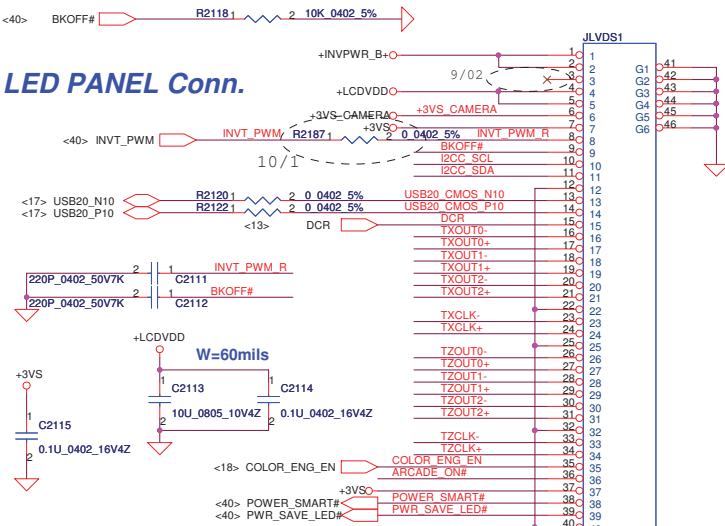
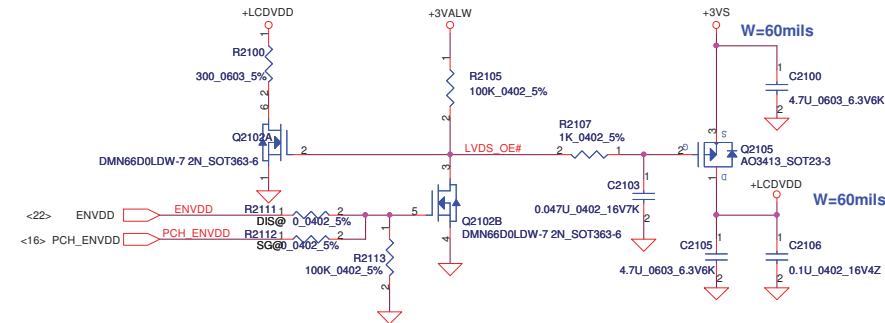
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/25	Deciphered Date	2010/08/25	Title	Mannhattan_Memory
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				Custom	1.0
Date:				Wednesday, October 27, 2010	Sheet 24 of 58





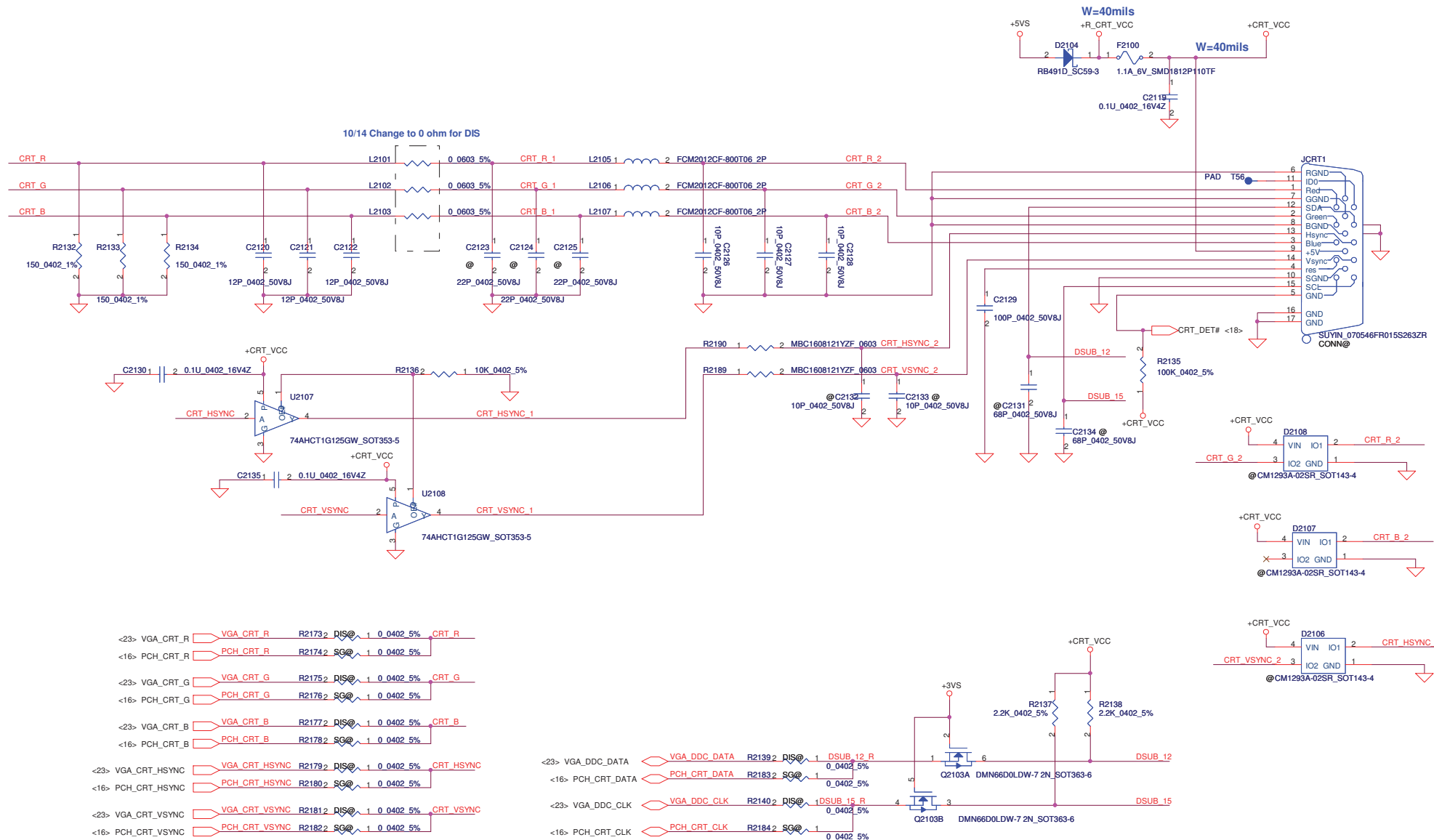


LCD POWER CIRCUIT



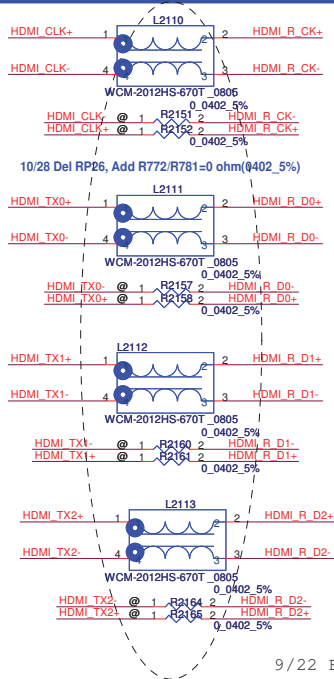
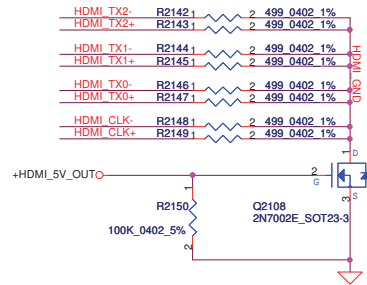
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/08/25	Deciphered Date	2010/08/25	Title	
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				Custom	LA-6931P
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				Rev	1.0

CRT Connector

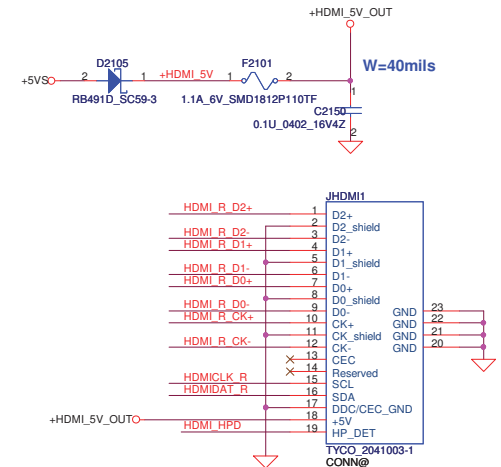
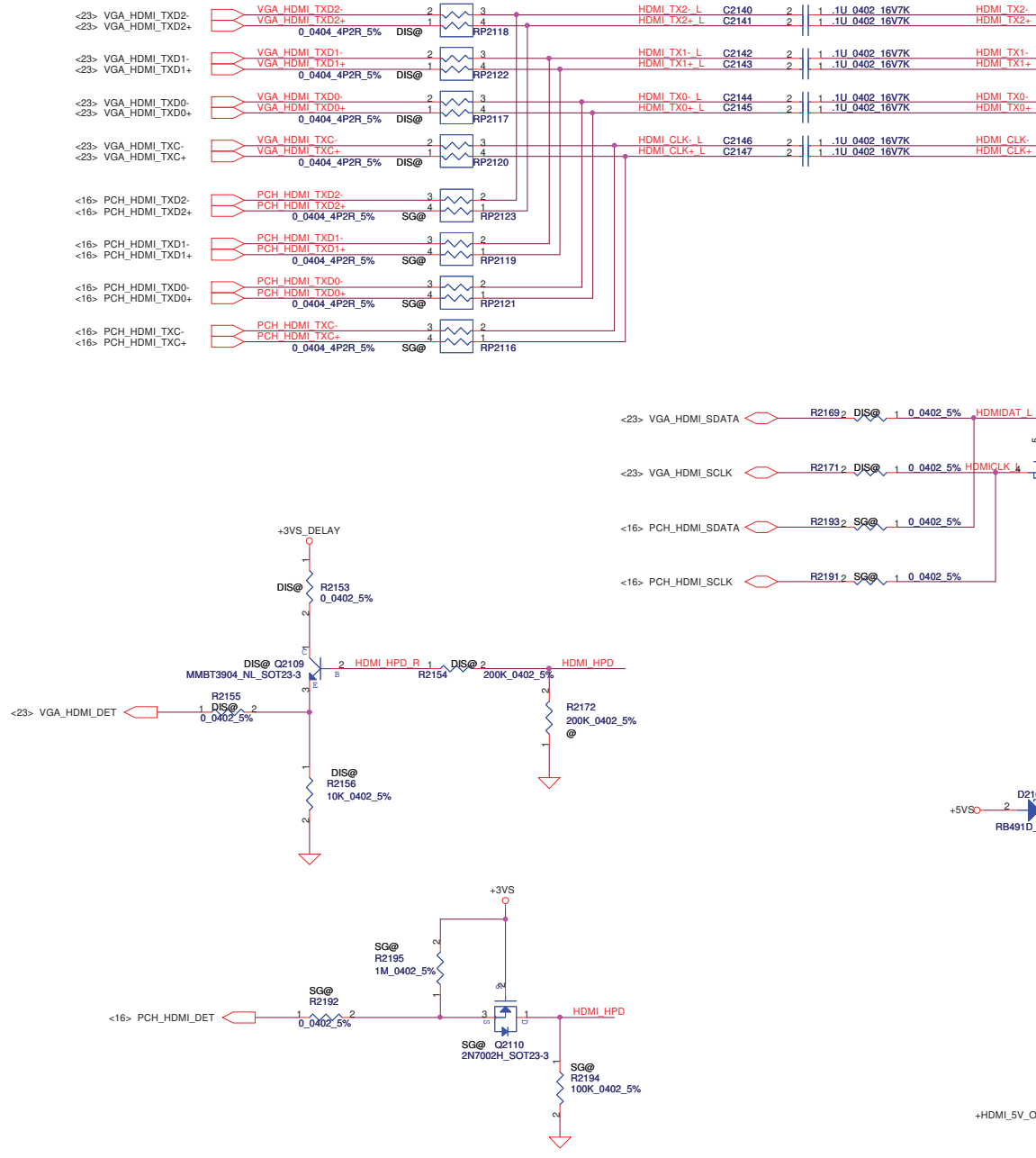


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				Date: Wednesday, October 27, 2010	Sheet 31 of 58

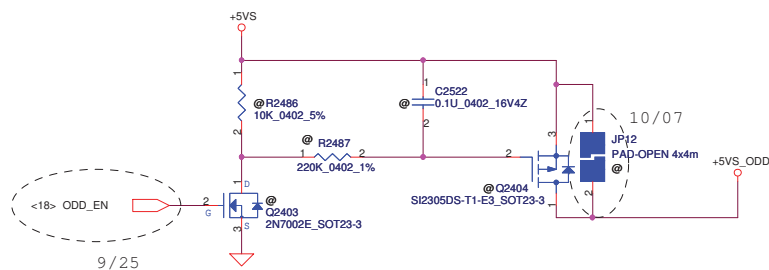
Place closed to JHDMI1



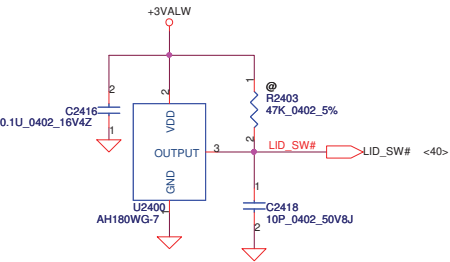
9/22 EMI



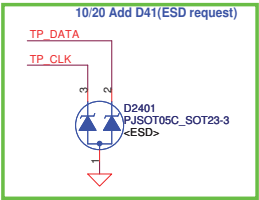
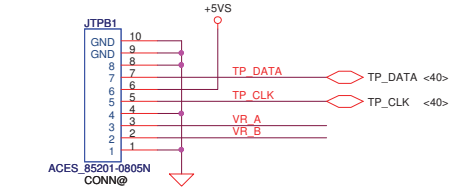
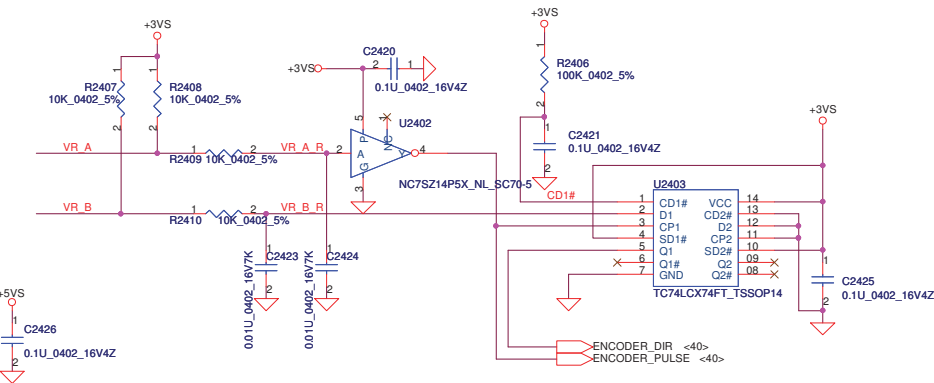
Security Classification				Compal Secret Data				Compal Electronics, Inc.			
Issued Date				2009/08/25				Title			
Deciphered Date				2010/08/25				HDMI Level Shifter & Conn			
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				Custom				LA-6931P			
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				58				Rev			
				1.0							



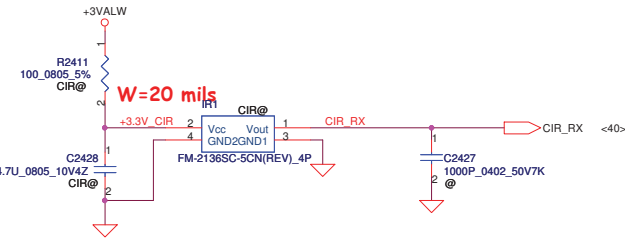
Lid Switch



Touch Pad

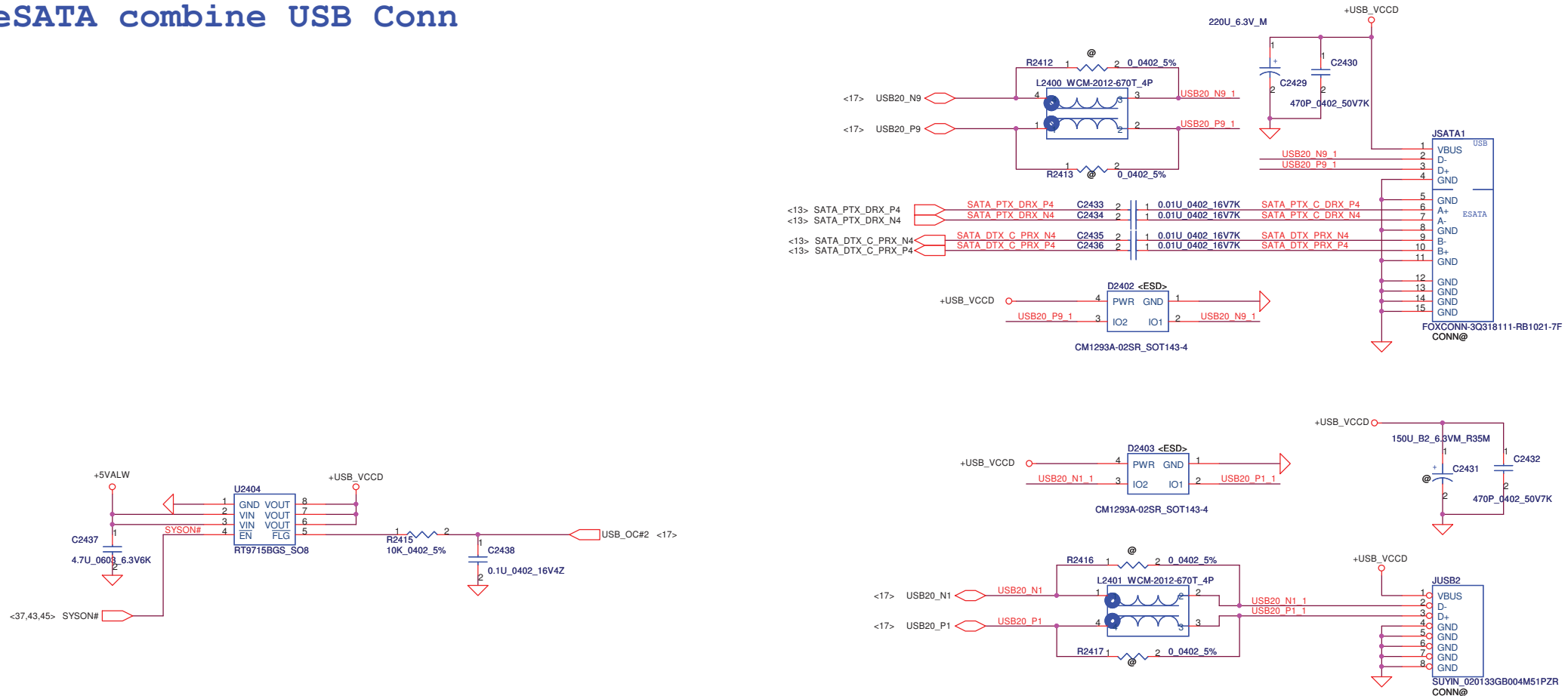


CIR

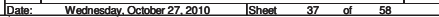


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				Size	Document Number	Rev
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eSATA combine USB Conn

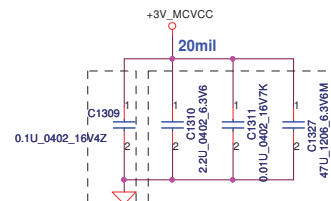
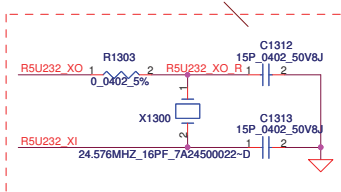
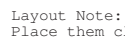


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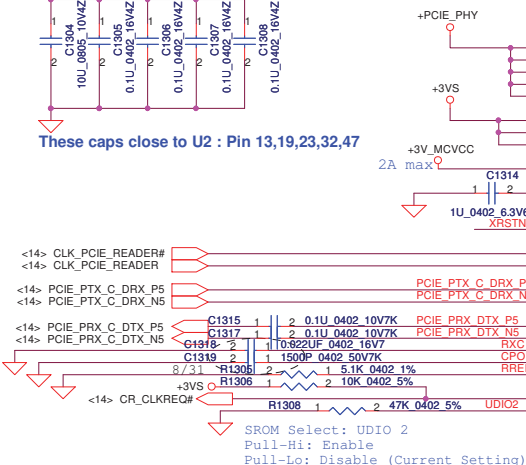




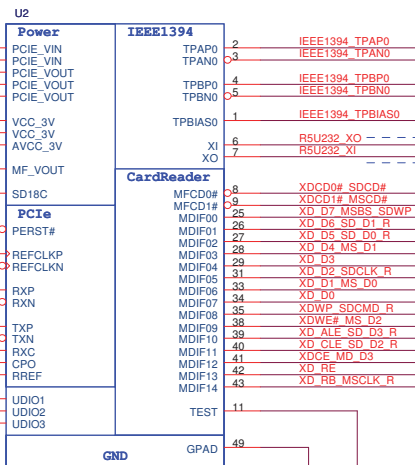
+3VS
40mil



Place them as close as possible to JREAD1
Place C1309 close to U2.36

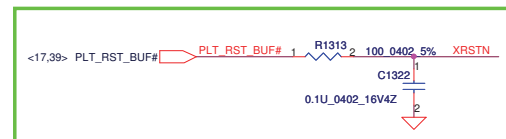
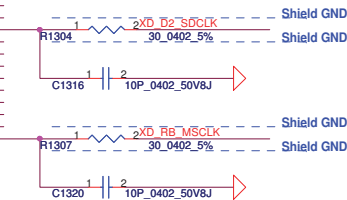


```
SROM Select: UDIO 2
Pull-Hi: Enable
Pull-Lo: Disable (Current Setting)
```

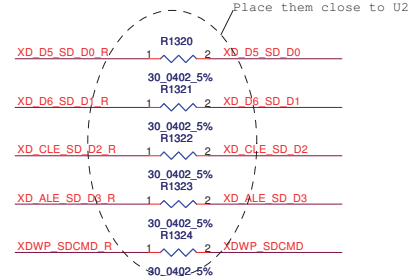


Layout Note:
Add GND shield for Xtal

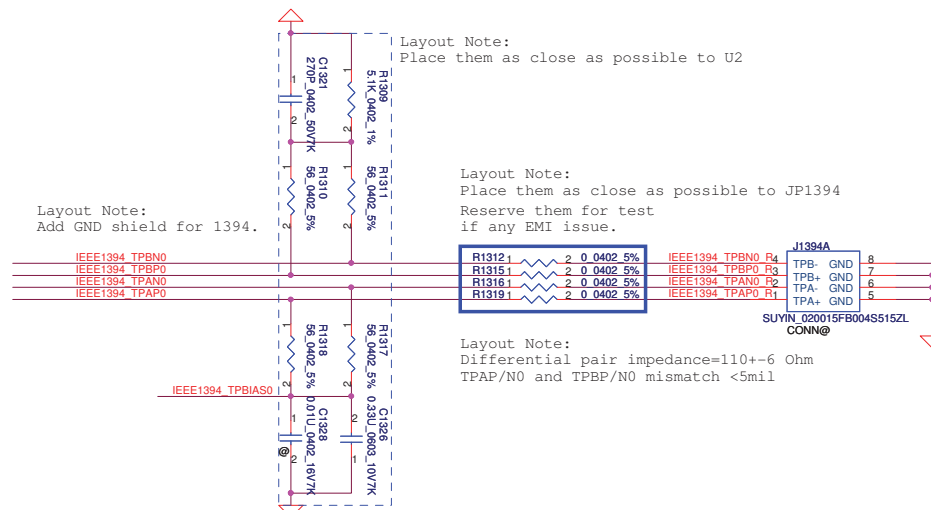
Impedance: 50 ohm (Microstrip)



Layout Note:
Place them close to U2



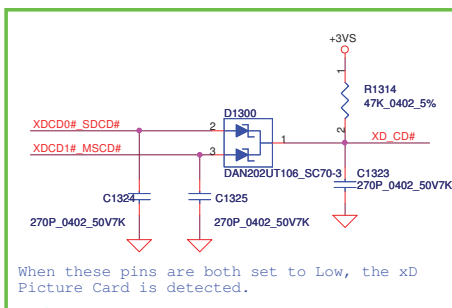
Layout Note:
Add GND shield for 1394.



Layout Note:
Place them as close as possible to JP1394
Reserve them for test
if any EMI issue.

Layout Note:
Differential pair impedance=110+/-6 Ohm
TPAP/N0 and TPBP/N0 mismatch <5mil

MFIO	SD8	MS8	XD
00	WP	BS	D7
01	D1	—	D6
02	D0	—	D5
03	D7	D1	D4
04	D6	D5	D3
05	CLK	—	D2
06	—	D0	D1
07	D5	D4	D0
08	CMD	—	WP#
09	D4	D2	WE#
10	D3	D6	ALE
11	D2	—	CLE
12	—	D3	CE#
13	—	D7	RE#
14	—	CLK	R/B#

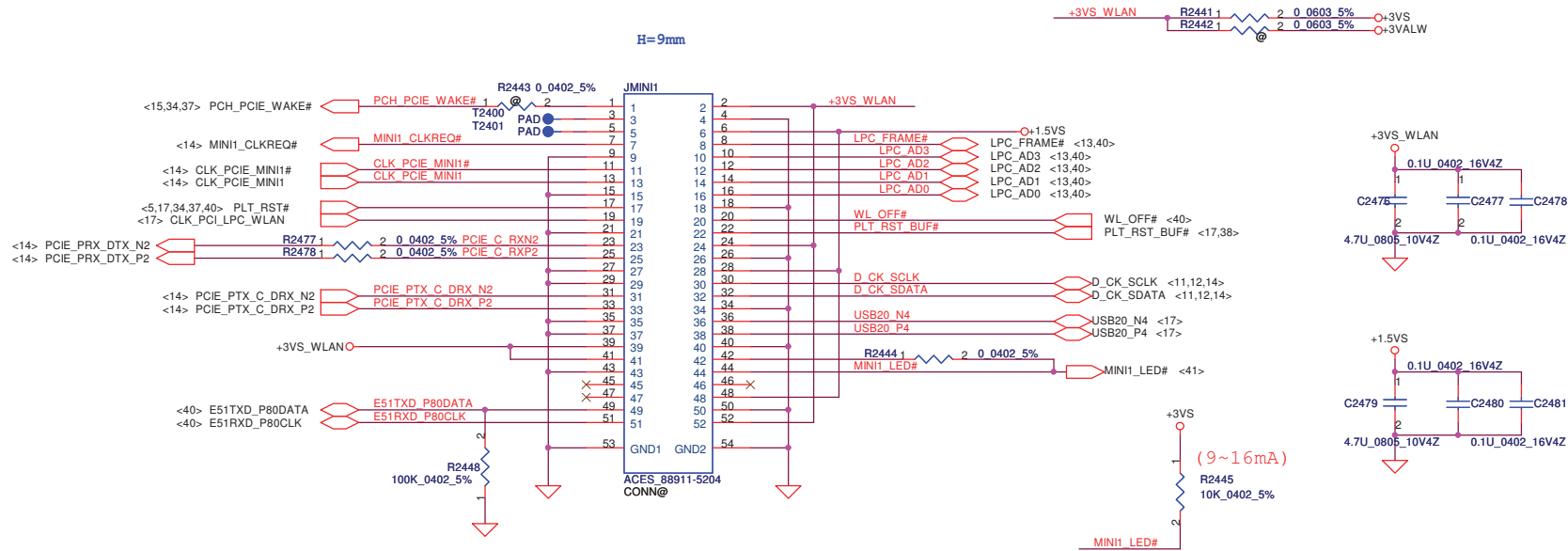


When these pins are both set to Low, the xD Picture Card is detected.

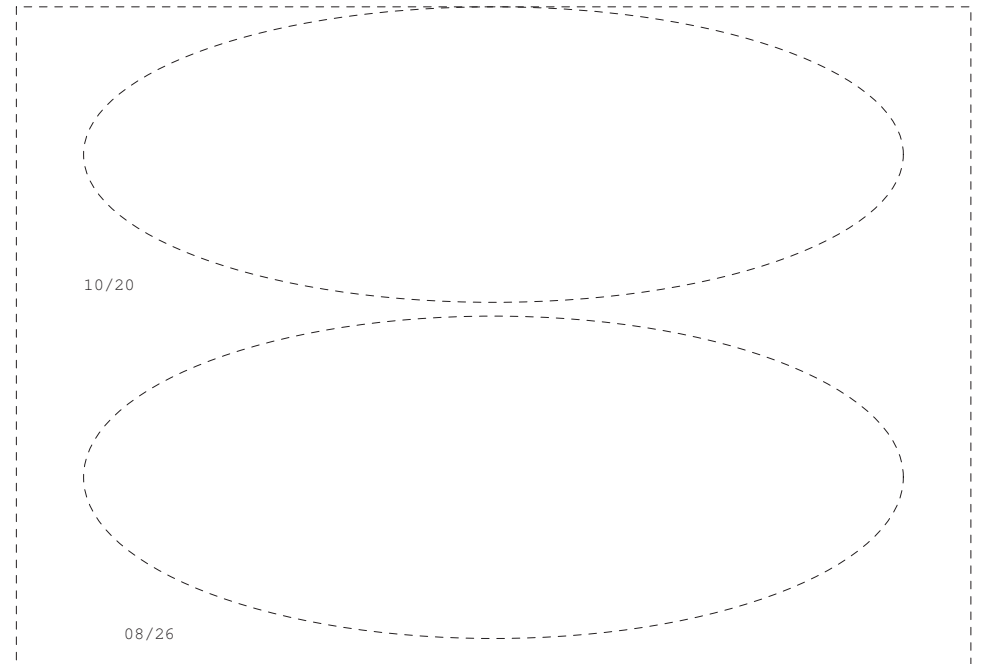
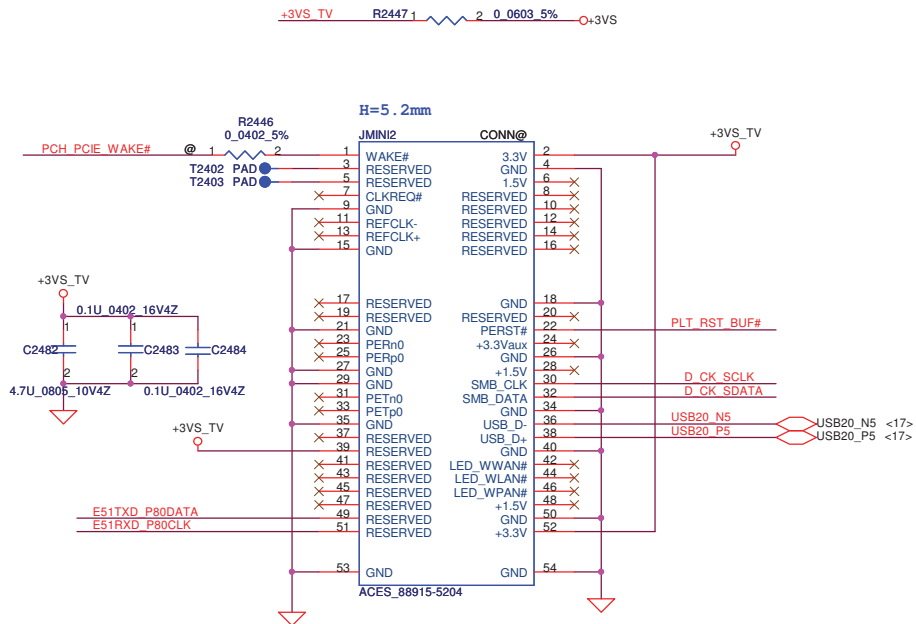
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Title			
R5U232			
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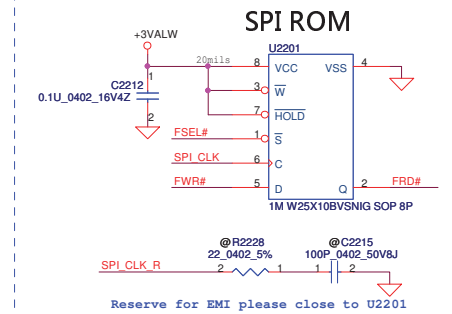
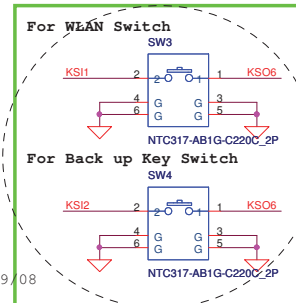
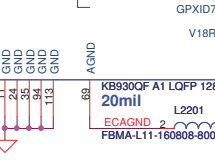
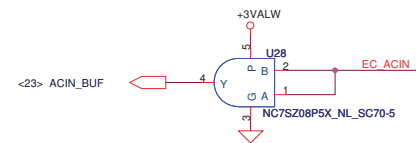
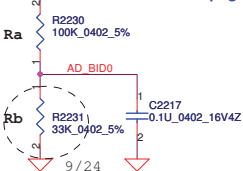
WLAN



MINI



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				Custom	1.0
				Document Number	LA-6931P
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				Sheet	39 of 58

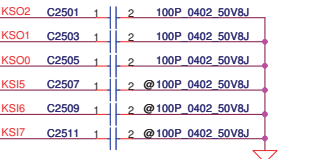
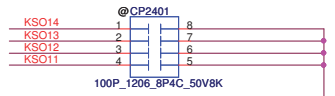
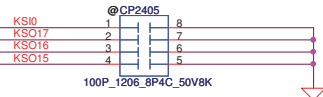
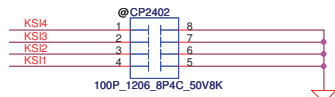
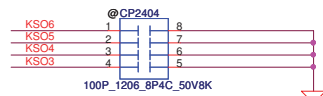
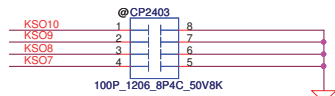
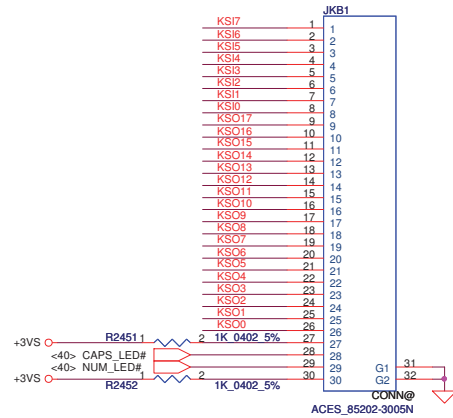


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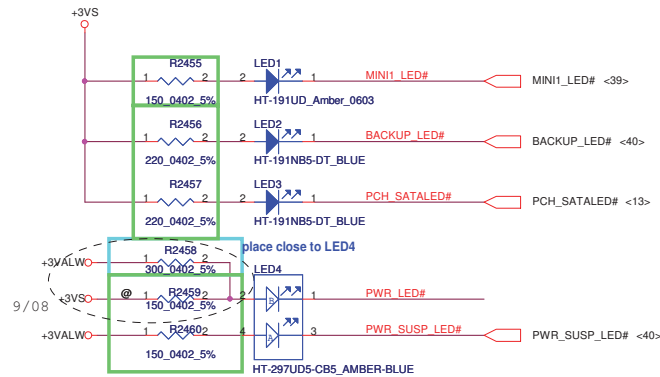
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Title			
EC ENE-KB930			
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INT_KBD Conn.

KSIO[0..7] <40>
KSIO[0..17] <40>

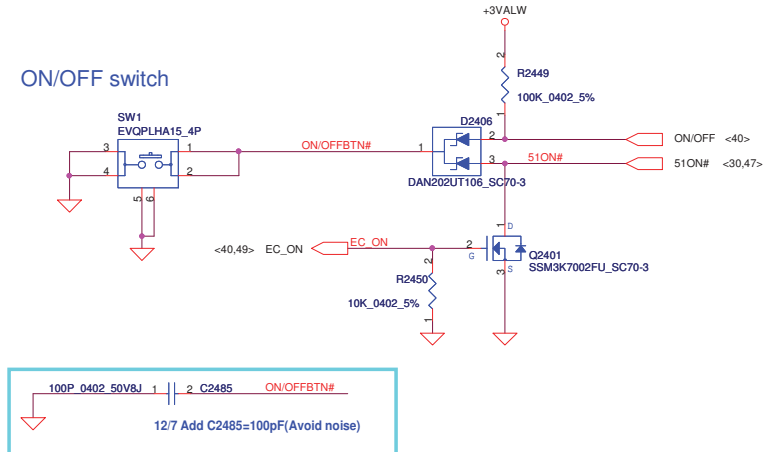


LED

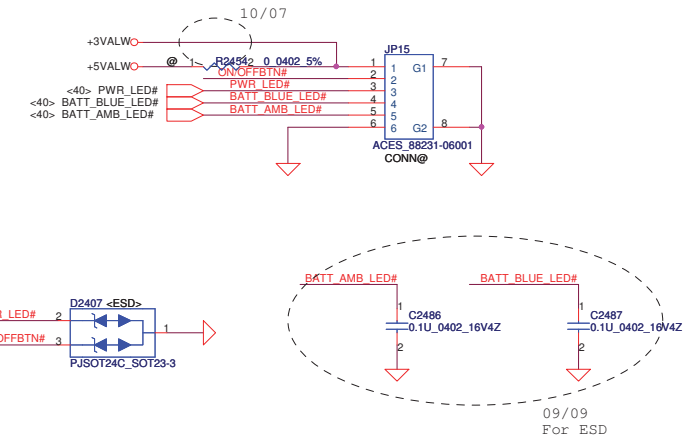


Power Button

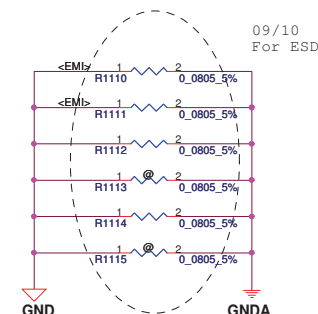
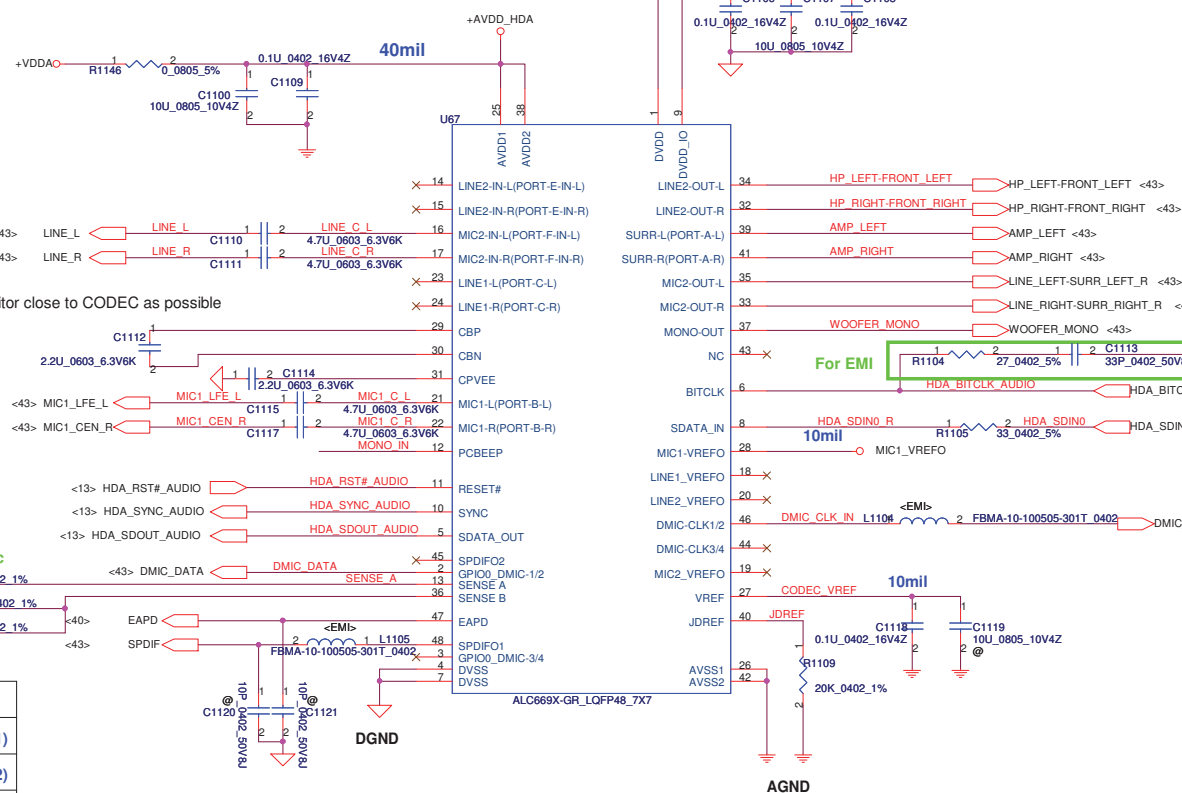
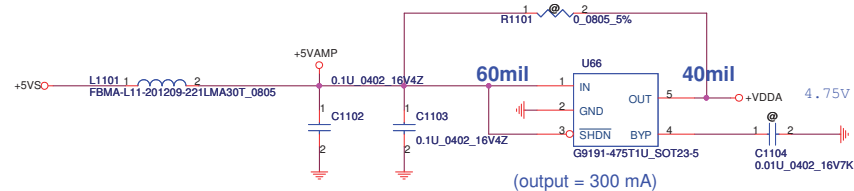
ON/OFF switch



Power Conn



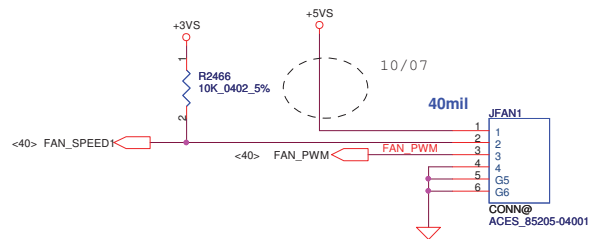
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Issued Date	2009/08/25	Deciphered Date	2010/08/25	Title
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Size B	Document Number	LA-6931P		Rev 1.0
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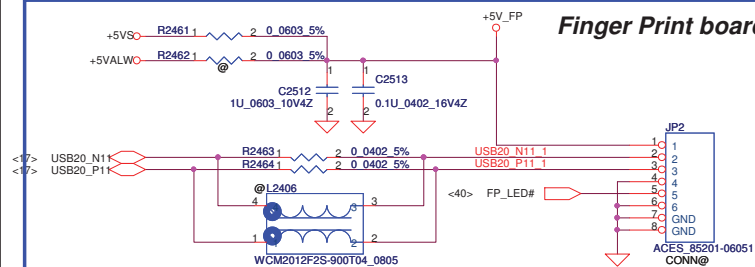
Sense Pin	Impedance	Codec Signals
SENSE A		PORT-A (PIN 39, 41)
	20K	PORT-B (PIN 21, 22)
		PORT-C (PIN 23, 24)
SENSE B	39.2K	PORT-E (PIN 32, 34)
	20K	PORT-F (PIN 33, 35)
		PORT-H (PIN 37)

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				Size B	Document Number LA-693IP	Rev. 1.
				Date:	Wednesday, October 27, 2010	Sheet 42 of 58

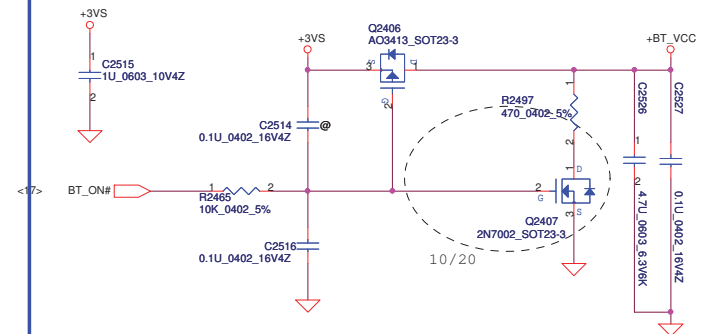
FAN1 Conn



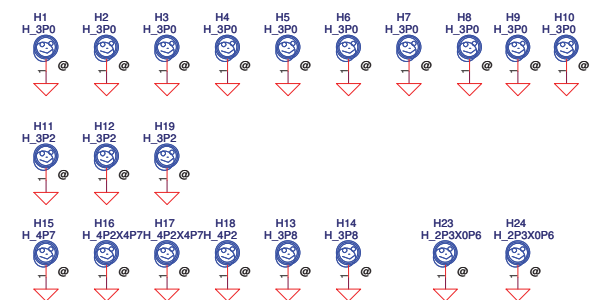
Finger Print board



BT



Screw



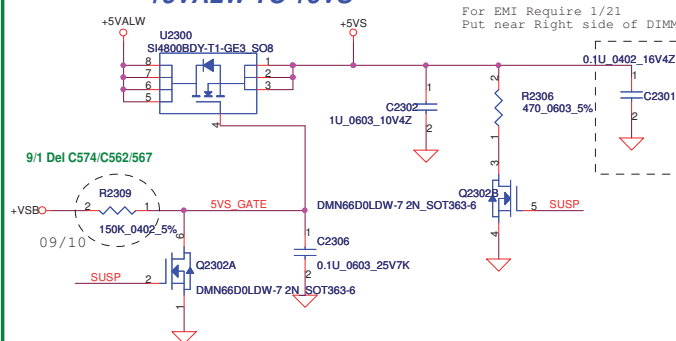
NON-PDH

2/25 Change footprint of H22

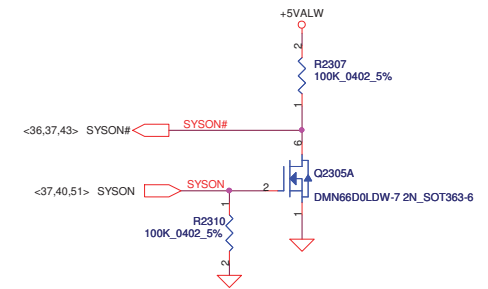
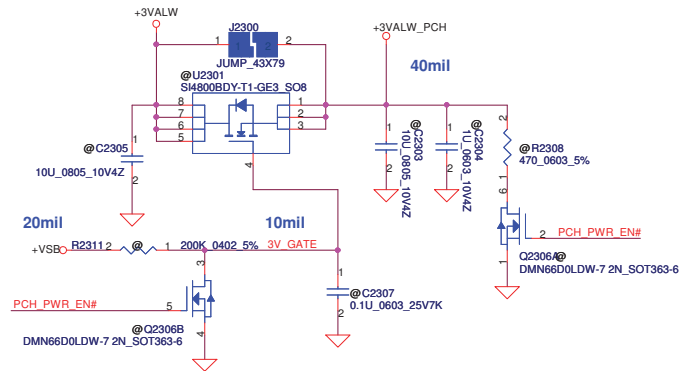


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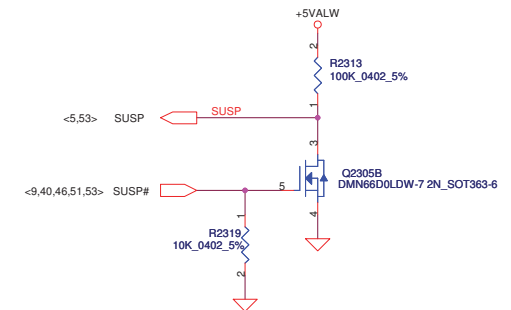
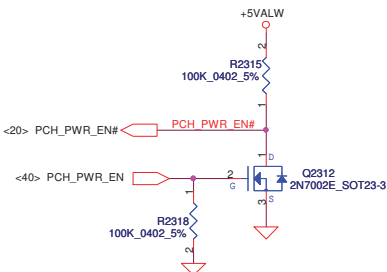
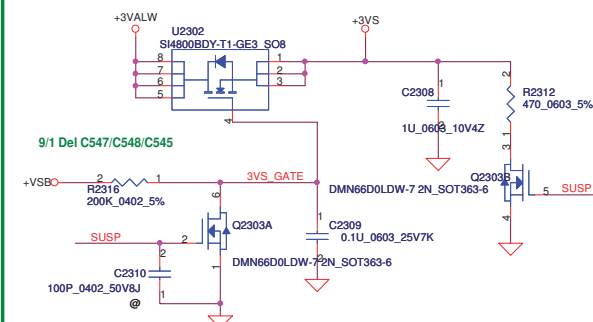
+5VALW TO +5VS



+3VALW TO +3VALW(PCH AUX Power) Short J5 for PCH VCCSUS3.3

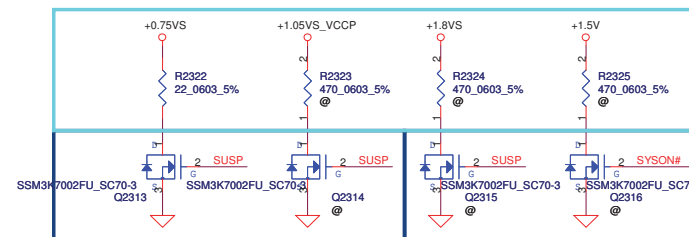
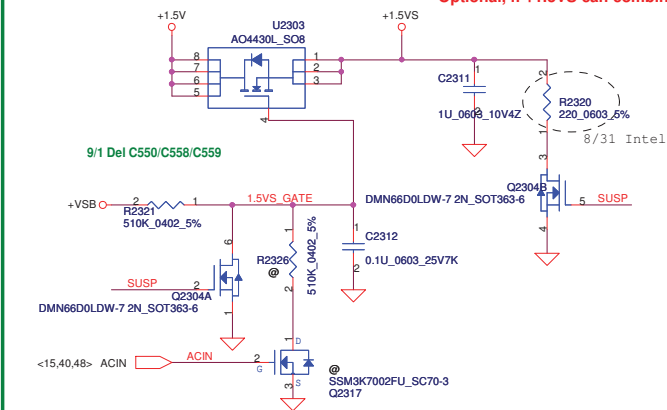


+3VALW TO +3VS



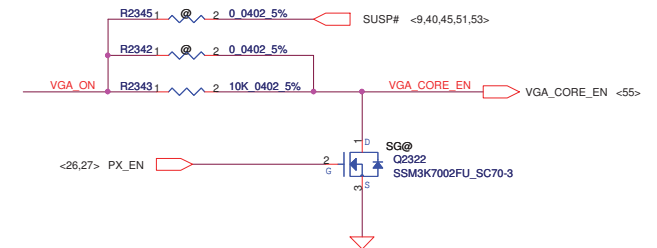
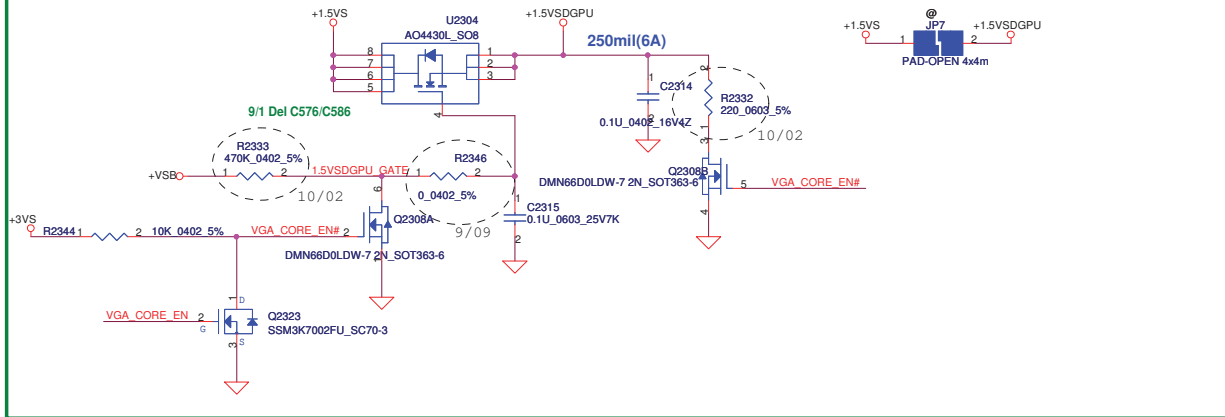
+1.5V to +1.5VS

Optional, if +1.5VS can combine with +1.5V_1

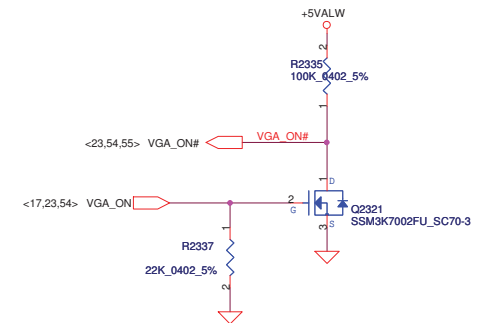
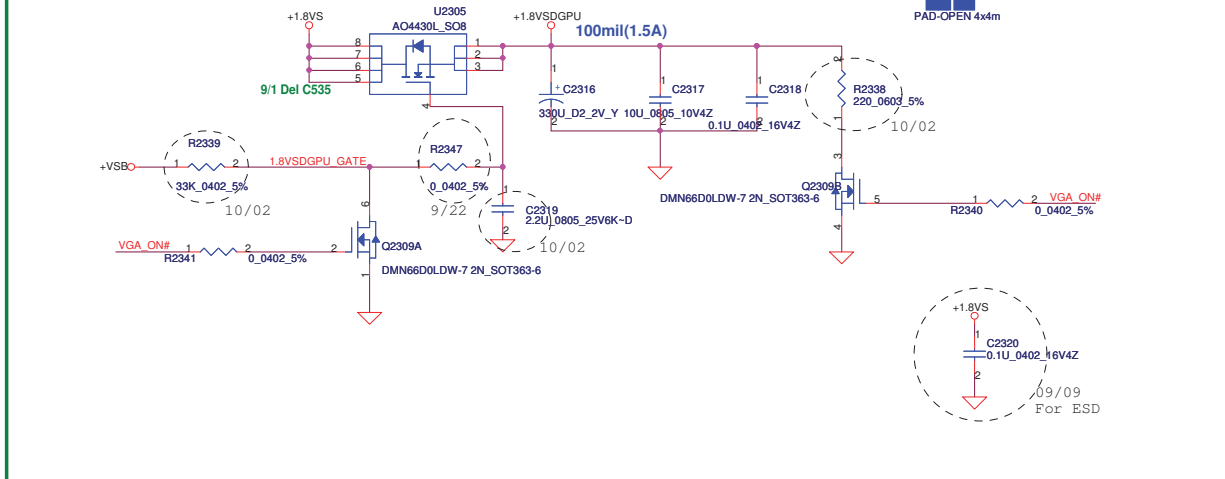


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						Size B	Document Number		Rev	
						LA-6931P	1.0			
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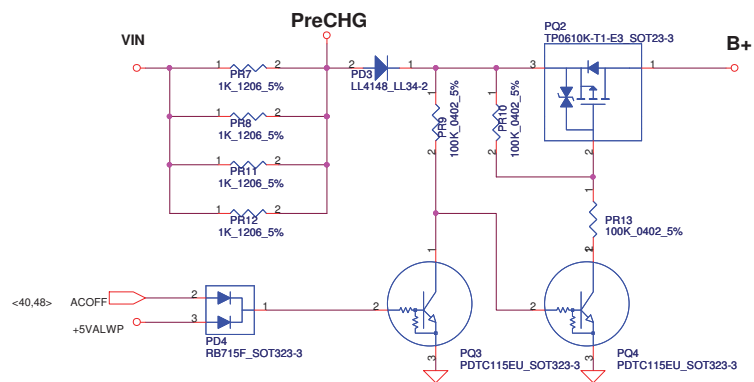
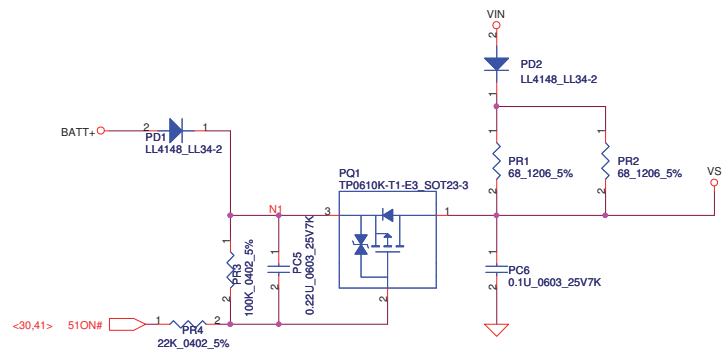
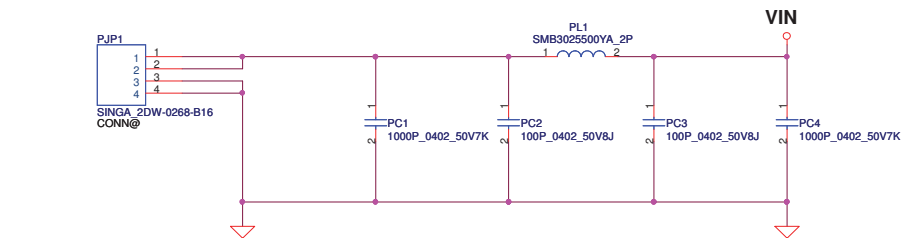
+1.5VS to +1.5VSDGPU Transfer



+1.8VS to +1.8VSDGPU Transfer



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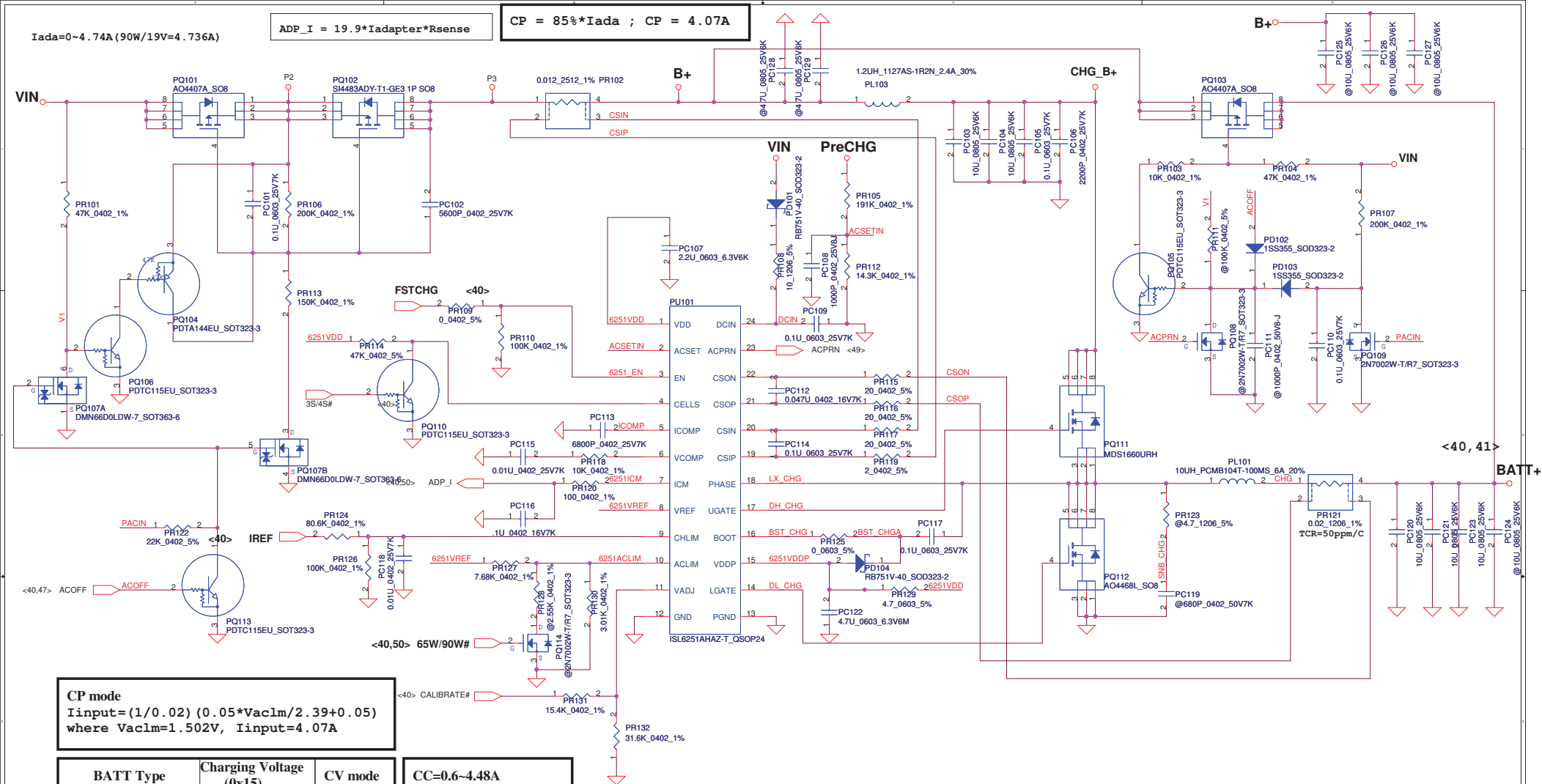


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Iada=0~4.74A (90W/19V=4.736A)

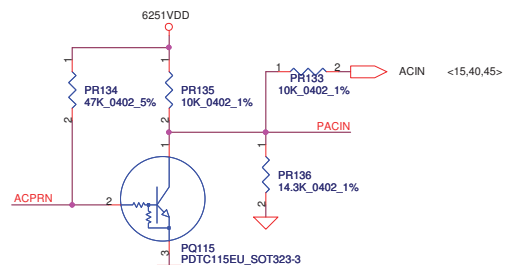
ADP_I = 19.9*Iadaprer*Rsense

CP = 85%*Iada ; CP = 4.07A

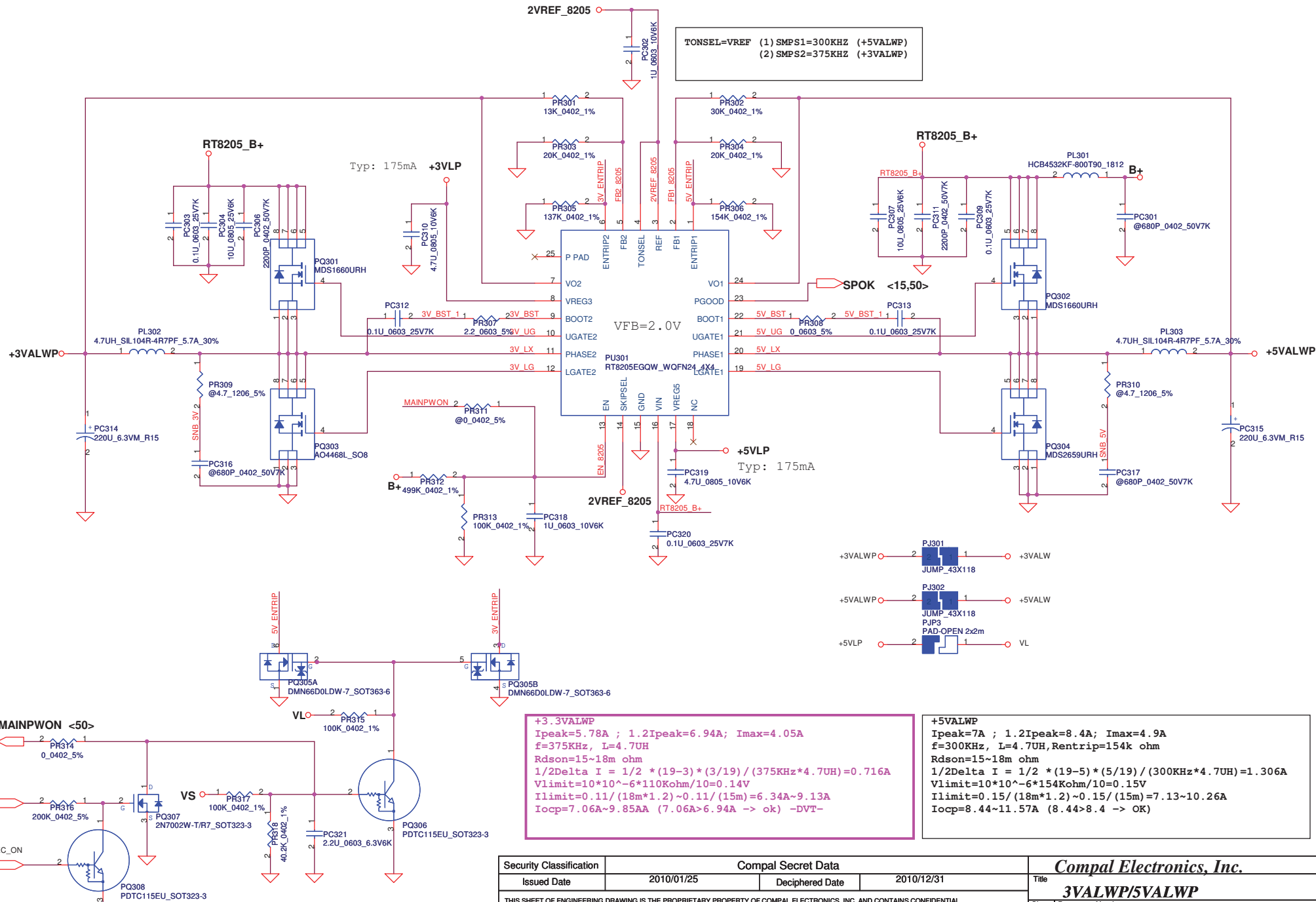


Ki
 $V_{chlim} = I_{ref} \cdot (PR374 / (PR372 + PR374))$
 $= I_{ref} \cdot (100K / (80.6K + 100K))$
 $= I_{ref} \cdot 0.5537$
 $I_{charge} = (165mV / PR369) \cdot (V_{chlim} / 3.3V)$
 $= (165mV / 20m) \cdot (1/3.3V) \cdot I_{ref} \cdot 0.5537$
 $= 1.3842 \cdot I_{ref}$
 $I_{ref} = 0.7224 \cdot I_{charge} \Rightarrow Ki = 0.7224$

Kv
 $R_{internal} = ic = 514K$ $R_{ec} = 3K$ $R1 = PR379 = 15.4K$ $R2 = PR381 = 31.6K$
 $R = 514K // 31.6K // (15.4K + 3K) = 11.372K$
 $r = 514K // 514K // 31.6K = 28.14K$
 $V_{cell} = 0.175 \cdot V_{adj} + 3.99V$
 $4.2V = 0.175 \cdot V_{adj} + 3.99V \Rightarrow V_{adj} = 1.2V$
 $V_{adj} = V_{ref} \cdot (R / (R + 514K)) + CALIBRATE \cdot (r / (r + 514K))$
 $1.1483 = CALIBRATE \cdot 0.6046 \Rightarrow CALIBRATE = 1.899$
 $1.899 = (4.2 - (V_{cell} + A \cdot 0.175)) \cdot Kv = (4.2 - (4.2 + A \cdot 0.175)) \cdot Kv$
 $A = V_{ref} \cdot (R / (R + 514K)) = 0.052$
 $Kv = 9.451$

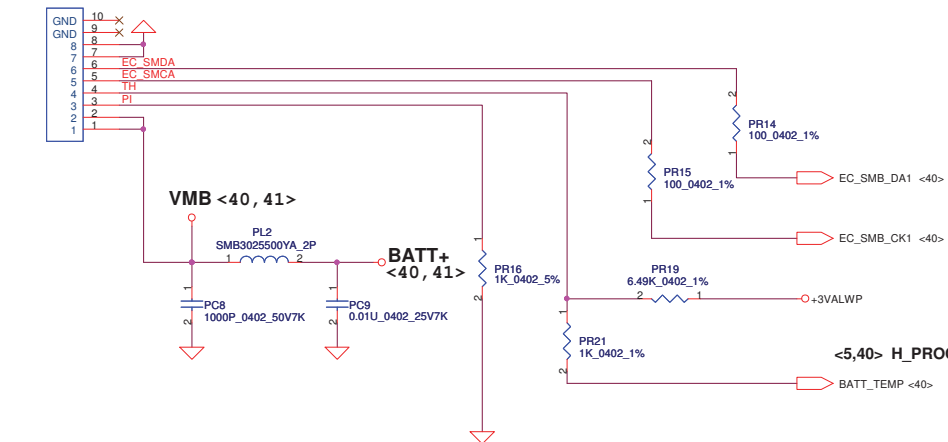


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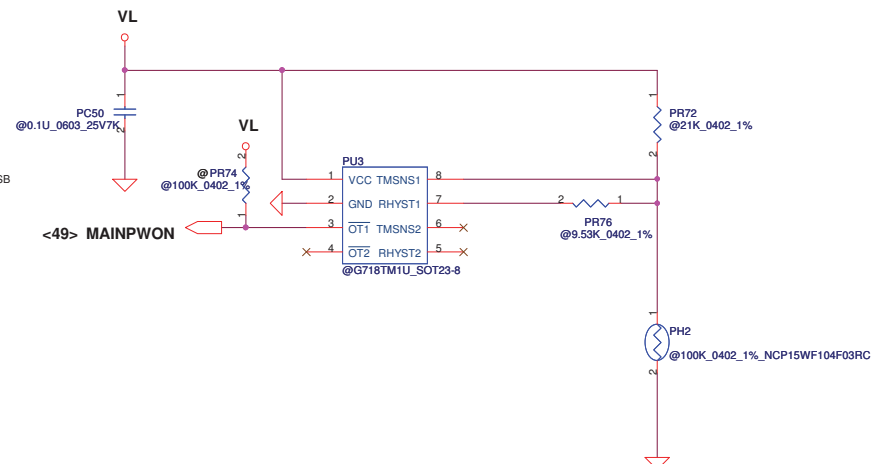
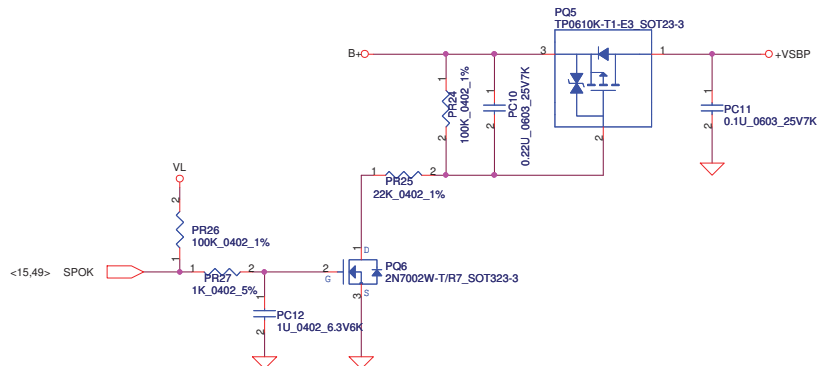
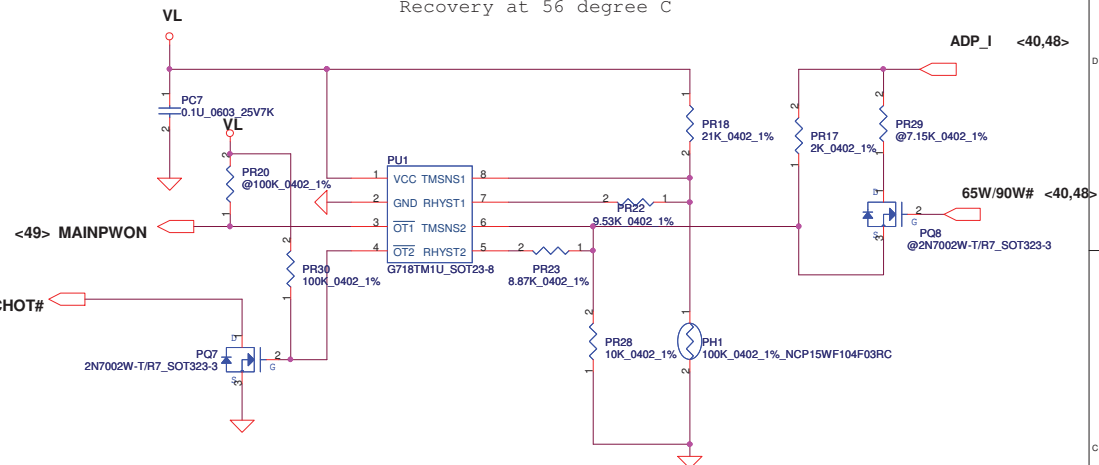


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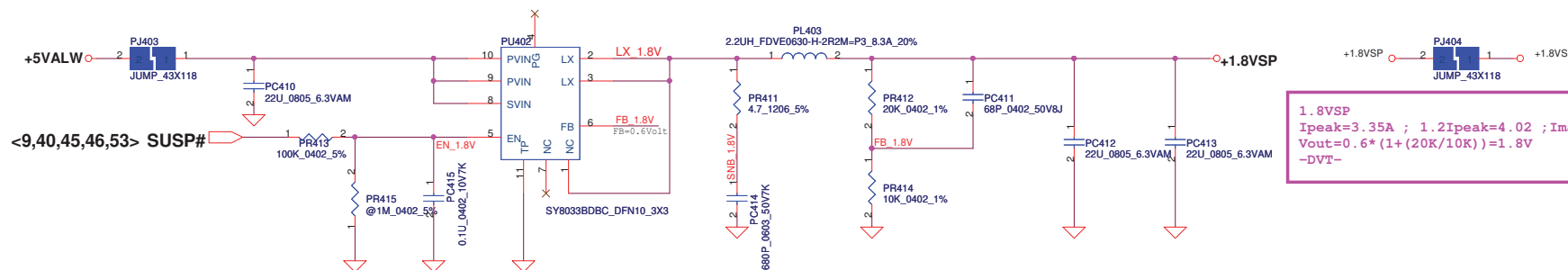
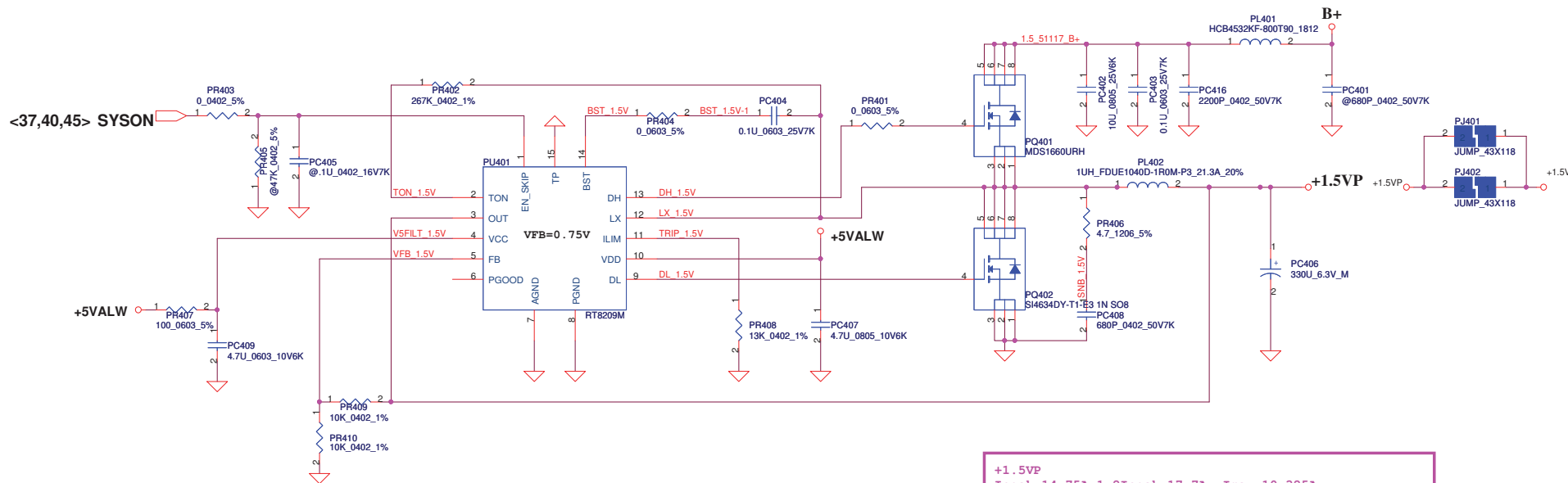
PJP2
SUYIN_200275GR008G13GZR



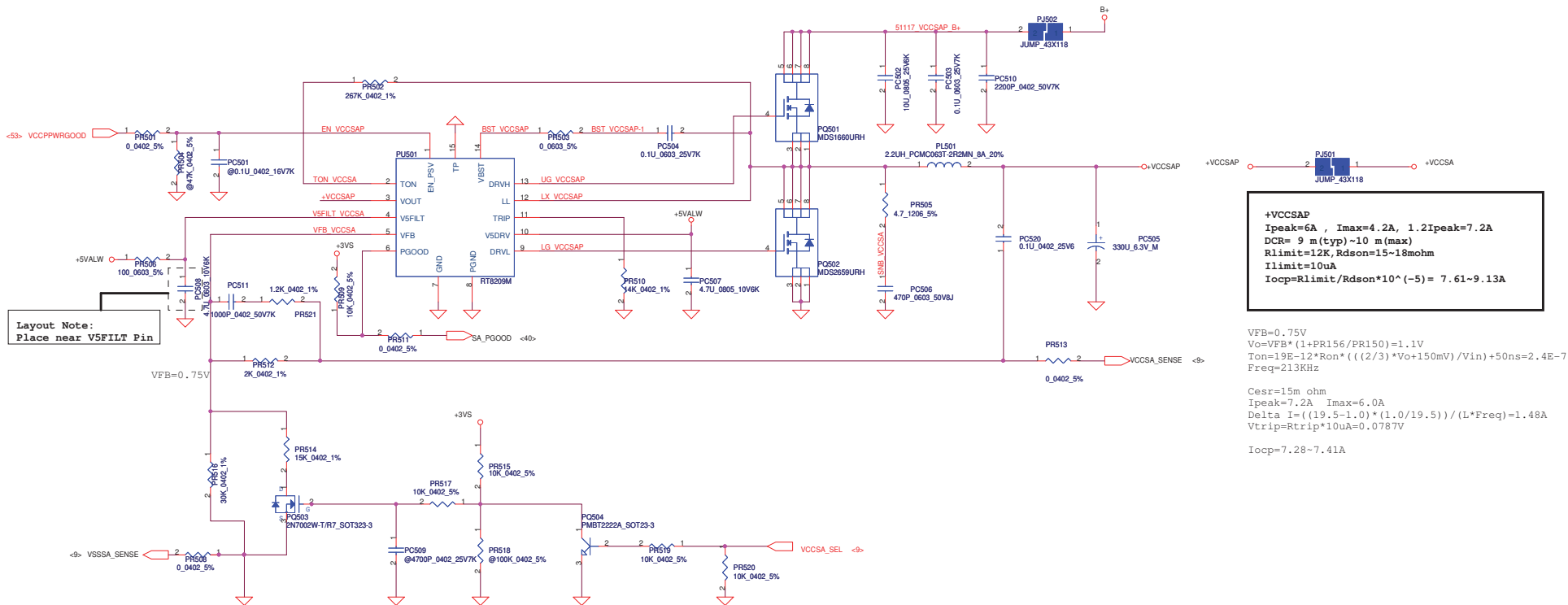
PH1 under CPU botten side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C



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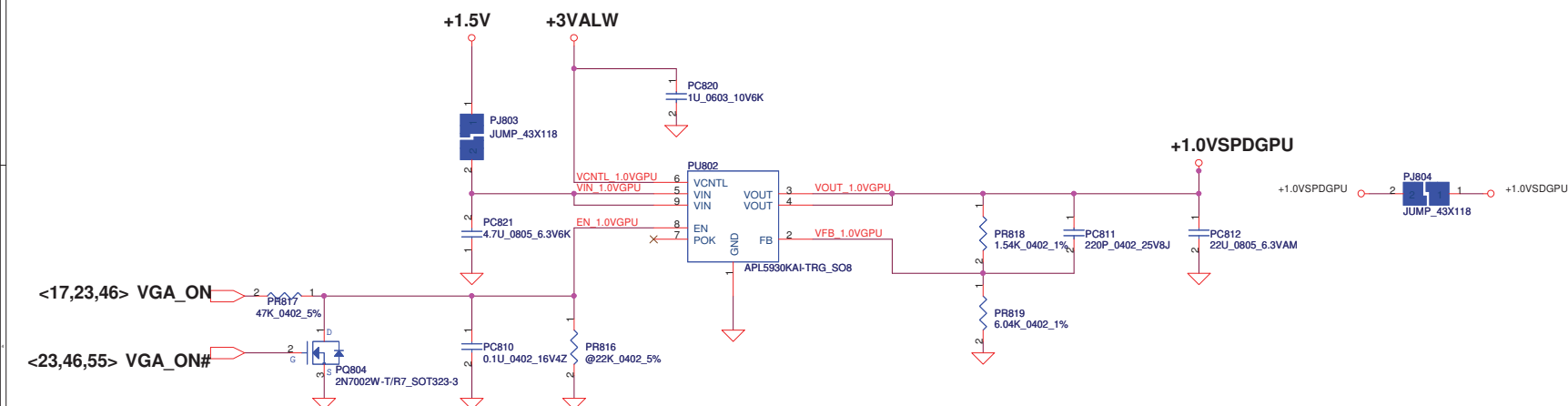
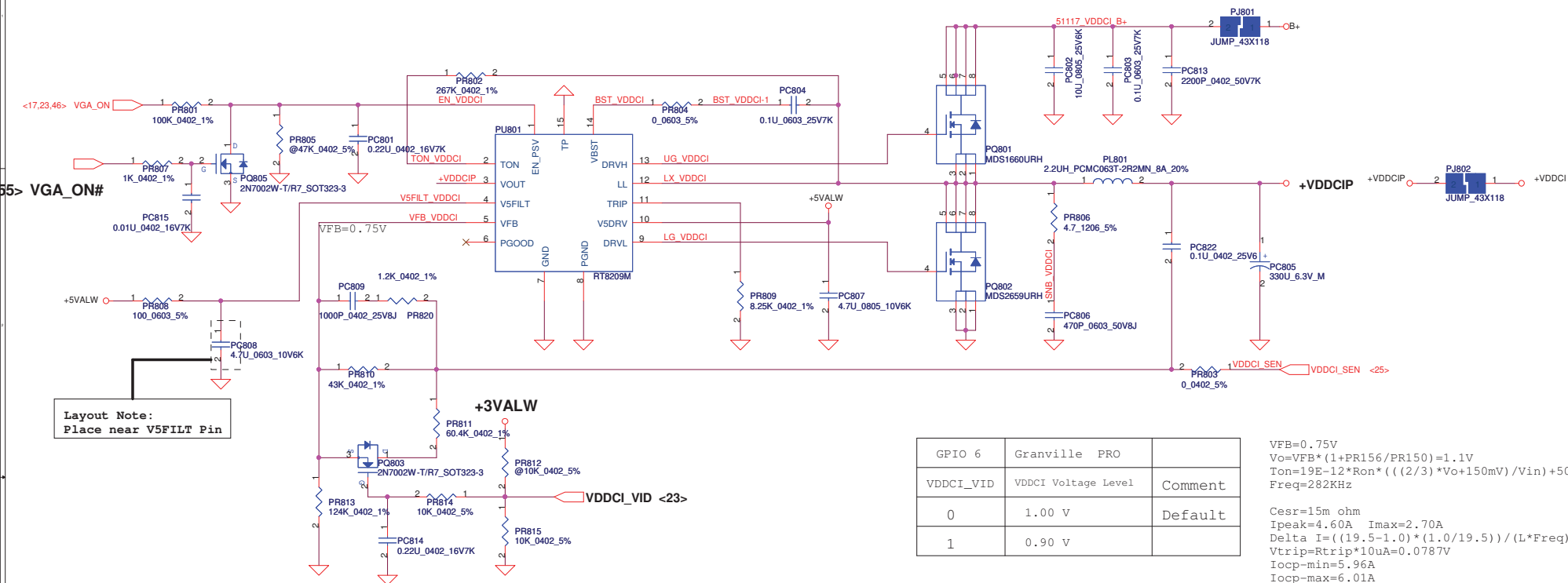
+VCCSAP
 $I_{peak}=6A$, $I_{max}=4.2A$, $1.2I_{peak}=7.2A$
 $DCR=9\text{ m}(\text{typ})\sim 10\text{ m}(\text{max})$
 $R_{limit}=12K$, $R_{dson}=15\sim 18\text{mohm}$
 $I_{limit}=10\mu A$
 $I_{ocp}=R_{limit}/R_{dson}\cdot 10^{(-5)}=7.61\sim 9.13A$

$V_{FB}=0.75V$
 $V_o=V_{FB}\cdot(1+PR156/PR150)=1.1V$
 $Ton=19E-12\cdot Ron\cdot(((2/3)\cdot Vo+150mV)/Vin)+50ns=2.4E-7$
 $Freq=213KHz$
 $Cesr=15m\text{ ohm}$
 $I_{peak}=7.2A$, $I_{max}=6.0A$
 $\Delta I= ((19.5-1.0)\cdot(1.0/19.5))/(L\cdot Freq)=1.48A$
 $V_{trip}=R_{trip}\cdot 10\mu A=0.0787V$
 $I_{ocp}=7.28\sim 7.41A$

VID[0]	VID[1]	VCCSA Vout	Require on 2011/2012	Required
0	0	0.9 V	Yes/Yes	Yes/Yes
0	1	0.8 V	Yes/Yes	Yes/Yes
1	0	0.725V	No/Yes	No/Yes
1	1	0.675V	No/Yes	No/Yes

Note:Use VCCSA_SEL to switch High & Low Level for VID[1]
 (i.e. VCCSA_SEL) due to the VID[0] is don't care for this setting.

the resister change
 from @ to pop component
 Add two jumpers on the HW's output cap of the
 +VCCSA's PIN(+) and PIN(-) to sense the
 feedback voltage for VCCSA_SENSE & VSSSA_SENSE.



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				2010/08/25				Size			
								Document Number			
								L/A-6931P			
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Parallel and tune length

C	Title	<i>PWR +CPU CORE/+VGFX CORE</i>
	Size Document Number	<i>LA-6931P</i>

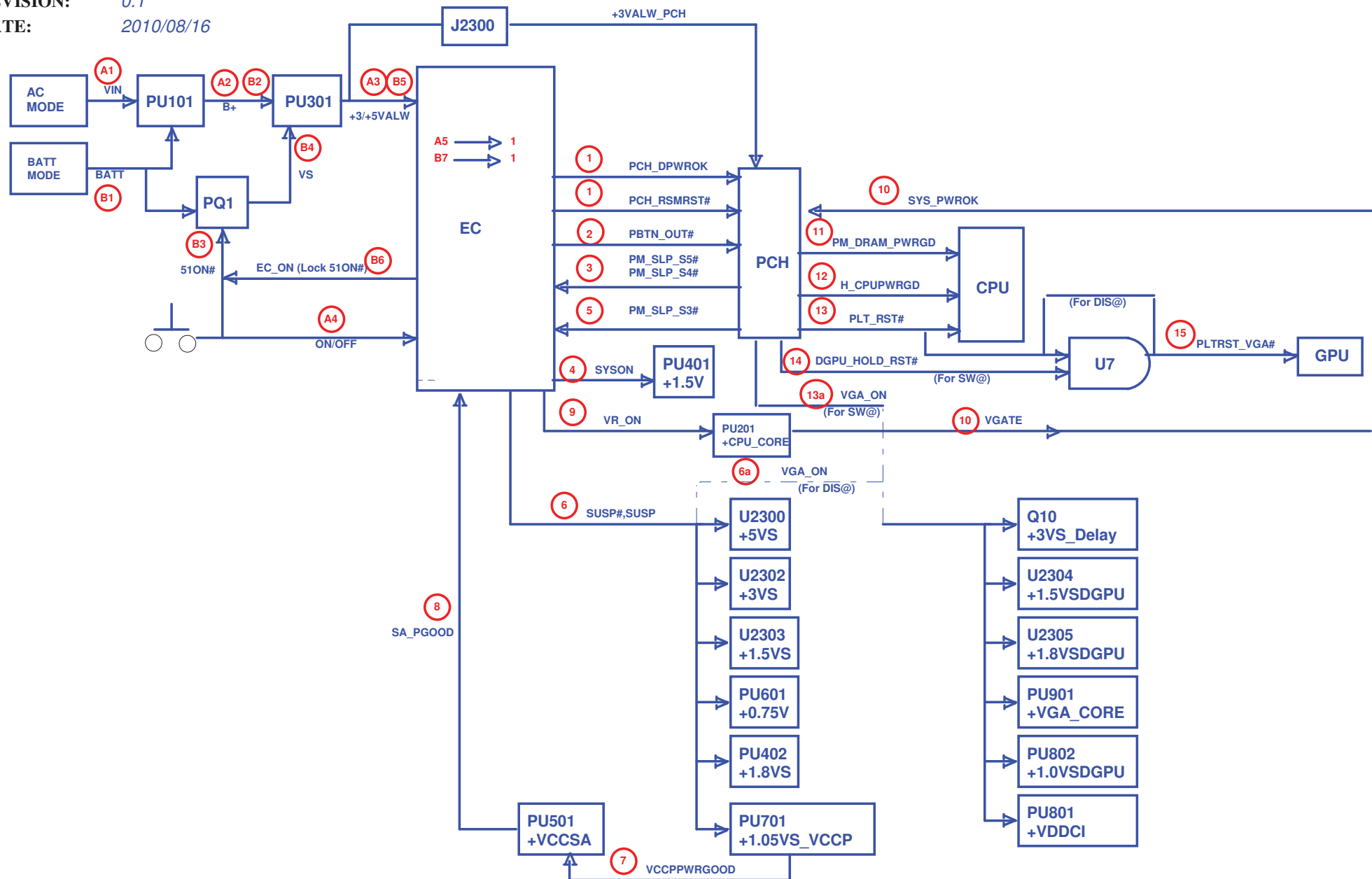
COMPAL CONFIDENTIAL

MODEL NAME: P5LM0 Power Sequence Block Diagram

PCB NAME: LA-6931P

REVISION: 0.1

DATE: 2010/08/16



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